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Principal,
J N N College of Engineering,
Nawale, Shivamogga - 577 204

Convener

Dr. A H Thejaswi
Professor and Head,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204

Co- Conveners

Dr. H B Suresh
Professor,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204

Dr. S Surendra
Professor,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204

* Link for joining the FDP program will be sent to the registered participants via Mail.

** e-certificate will be provided for all the participants.

How to register

https://forms.gle/[illegible]

click or scan to connect

Last Date for Registration

22th August 2020

Bank Details

- Account Name - Principal, JNNCE, Shimoga
- Account Number - 6322201000004
- Bank - Canara Bank
- IFSC code - CNRB0006322
- Branch - JNNCE Campus Branch, Shimoga

Note

- Participants has to pay a registration fee of 100/- Rs.
- Proof of copy has to be uploaded in the same form (above).

For further details contact

Organizing Coordinators

Mr. N G Ajjanna
Assistant professor,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204
+91 98863 95389
nga_ee@jnnce.ac.in

Mr. K B Veeresha
Assistant professor,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204
+91 85182 77257
veeresha-kb@jnnce.ac.in

Mr. Vidyashankar M
Assistant professor,
Department of E&E Engineering,
J N N College of Engineering,
Nawale, Shivamogga - 577 204
+91 [illegible]
vsm_ee@jnnce.ac.in

National Education Society (R)

J.N.N COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Certified by ISO 9001:2008, Accredited by NAAC, NBA Accredited Number, UG, PG, PhD, Recognized by Govt. of Karnataka, Affiliated to VTU, Belagavi)
Kuvempu nagar, Nawale, Shivamogga, 577204, Karnataka
[illegible]
www.jnnce.ac.in

Internal Quality Assurance Cell (IQAC)

Department of E&E Engineering

In association with.

The Institute of Electronics & Telecommunication Engineers

Organizing. one week online

FACULTY DEVELOPMENT PROGRAM

RECENT TRENDS & EMERGING TECHNOLOGIES IN DISTRIBUTED GENERATION SYSTEMS

10:30 AM TO 12:30 PM

24th August 2020 to 28th August 2020

About NES

- Established in 1946 by freedom fighters and philanthropists of high repute, with a mission "to provide quality education at a reasonable cost" giving emphasis on rural and women education.
- Has set up 35 + educational institutions in the Malnad region of Karnataka state, in a wide range of disciplines such as Engineering, Science, Pharmacy, Commerce, Humanities, Education, Law, Business Administration, Physical education, Computer application and Applied Sciences.
- The management is comprising of highly respected persons from different professions and social service committed to carry forward the enlightening the vision and the illustrious legacy of its founders.

About JNNCE

JNNCE was established in the year 1980 by the National Education Society. The college is affiliated to Visvesvaraya Technological university (VTU), Belgaum and recognized by AICTE, New Delhi. Institution is NAAC accredited and UGC 2f and 12B certified. It offers 7 UG (all the branches are NBA Accredited upto 30-06-2022) and 7 M.Tech program besides MBA and MCA. It has 7 research Centers recognized by VTU. Spread over 52 acres, the campus has all the amenities to cater to the needs of about 3500 students and 400 faculty and staff. All the departments have good infrastructure facilities with highly qualified and experienced faculty, staff and well equipped laboratories. The college is regarded as one of the excellent centers for technical education and research. JNNCE is ranked 34th among top Engineering colleges in India and 7th in Karnataka as per outlook survey 2019. The college is providing its fruitful services in the 35th year. The college organizes international and national workshop, conferences, seminars and short term courses on a regular basis. The institution has IEEE as well as ISTE memberships.

About E&E Engineering Department

- The department of Electrical & Electronics Engineering was established in the year 1980 with sanctioned intake of 25 affiliated to University of Mysore till 1990.
- The intake was enhanced to 40 during 1994 and 60 during 1997 during which the program was affiliated to Kuvempu University. Presently the program is affiliated to VTU from 1998 onwards.
- Graduates of this program have acquired M.Tech/MS/PhD programs in country & abroad.
- Alumnis are in Academic, Industries and government bodies like KPTCL/KPC etc. Department provides consultancy services to local organizations and industries and undertakes Electrical maintenance work of JNNCE campus and hostels. In 2014 the department started M.Tech in Power Electronics course with sanctioned intake of 18 under VTU.

About FDP

Focus of the Programme

- Distributed generation (DG) is an approach that employs small-scale technologies to produce electricity close to the end users of power.
- DG technologies is in nascent of modular generation and they offer a number of potential benefits. In many cases, distributed generators can provide lower-cost electricity and higher power reliability and security with less environmental consequences than centralized power generators.
- Grid synchronization is one of the key issues for distributed power generation system. In recent years, an increasing number of new energy generation systems are connected to the grid. This greatly affects stability of the grid, which brings difficulty for inverters to be connected to the grid. So accurate detection of voltage, frequency, positive and negative sequence components under complex and unbalanced grid faults becomes one of the key issues of control of grid-connected inverters.
- FDP is as planned to impart research skills to the researchers and teachers to improve the quality of research in the area of distributed Generation.
- This programme will bring a positive transformation among the faculty members towards research work and enable the participants to develop competence in understanding the recent advances in distributed generation systems. The eminent personalities from industry and institutes are invited to give presentation.

Scope of the Programme

- Participants will be able to work in the field of "RECENT TRENDS & EMERGING TECHNOLOGIES IN DISTRIBUTED GENERATION SYSTEMS" and it's applications.

Resource Persons

Dr. Dattatreya N Gaonkar
Associate Professor,
Department of E&E, NITK Surathkal

Dr. Suresh H Jangamshetti
Dean R&D, Professor and Head,
Department of E&E, BVB, Bagalkot

Dr. C K Sreepathi
Assistant Vice president & Energy consultant,
JNNCE, Shivamogga

Mr. Shankar Sharma
Power policy analyst,
Bangalore - Karnataka

Dr. Shankar C B
Professor,
Department of E&E Engineering, NIT, Surathkal

Dr. Chidanandappa R
Associate professor,
Department of E&E, NIT, Surathkal

Who Can Register ?

- Faculties of Engineering (EEE, ME & ECE)
- Scientists of R&D institutes
- Researcher Scholars.

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(26) Lecture delivered.

||Jai Sri Gurudev||

Sri Adichunchanagiri Shikshana Trust (R)

SJB INSTITUTE OF TECHNOLOGY

No. 67, BGS H & E City, Kengeri, Bengaluru-560060.

Accrediated by NAAC with 'A' Grade

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Certificate of Appreciation

IEEE

To

Dr. Shankaralingappa C B

In appreciation of the contribution as a speaker for a Faculty Development Program on ***"FUTURE ENERGY TRENDS AND Its Impact"*** *, From 24th to 28th August 2020 conducted by* ***DEPARTMENT OF EEE, SJBIT.*** *We whole heartedly Thank you for the your valuable time and efforts in making the programme a grand success. We value your concern and support extended sir.*

Dr. Babu N.V
Convener / HOD, EEE

Dr. Puttaraju
Principal

Sri Krishnadevaraya Educational Trust

SIR M. VISVESVARAYA INSTITUTE OFTECHNOLOGY

Krishnadevaraya Nagar, Hunasamaranahalli, International Airport Road, Bengaluru – 562 157,
(Affiliated to Visvesvaraya Technological University, recognized by AICTE & Accredited by National Board of Accreditation New Delhi, An ISO 9001:2008 Certified institution)
Phone: 080-28467248, 28477024/25/26 Fax:080-28467081
Email:principal@sirmvit.edu, sirmvitbgl@gmail.com

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

EEE/AICET/FDP/2020/09a 07/12/2020

To,
Dr. Shankar
Professor, Electrical and Electronics Engineering Department
Dr. AIT., Bangalore Urban, Karnataka

Dear Sir,

Subject: Invitation for AICTE sponsored Faculty Development Program on "Renewable Energy Forecasting India Scenario and Future Prospects " as **Speaker**, to be held at SIR MVIT EEE dept. on **10th Dec 2020 from 2.00 pm to 3.30 pm**:- Reg.

With reference to the above subject, we feel it is immense pleasure to invite yourself to be part of our technical session as resource persons on **10th Dec 2020 from 2 pm to 3.30 pm** for the TWO week AICTE sponsored Faculty Development Program, organized by SIR MVIT on the topic "Renewable Energy Forecasting India Scenario and Future Prospects ".

This two week AICTE FDP is conducted from 7th Dec 2020 to 19th Dec 2020. This FDP is attended by around 30 to 40, Faculty and Research Scholars from various Engineering Colleges across the country. This FDP intends to educate and upgrade the knowledge of the faculty members and Research Scholars about the recent development and practical insight about the RE.

In view of your expertise and experience in RE field, we would look forward your presence and share your vast knowledge to all the participants to enrich them.

Kindly acknowledge your presence via email to dr.hlsuresh_eee@sirmvit.edu

Dr. H L Suresh
Prof & HOD-EEE
FDP Chief Coordinator

26 /20-21

Panchajanya Vidya Peetha Welfare Trust (Regd)

Dr. Ambedkar Institute of Technology

(An Autonomous Institution, Aided by Government of Karnataka
Affiliated to Visvesvaraya Technological University, Belgum & Approved by AICTE New Delhi)
BDA Outer Ring Road, Near Jnana Bharathi Campus, Mallathally, Bengaluru - 560 056

Ref. No. I.T./468/2020/21

Date : 05-03-2021

LETTER OF APPRECIATION

We highly appreciate the effort of **Dr. Jyothi P Koujaligi**, Professor, Department of EEE for being the resource person in Faculty Development Program on **Outcome based Education and NBA Accreditation** sponsored by AICTE – MARGDARSHAN (Share and Mentor Institutions) Scheme, held during 22nd to 27th February 2021. Dr. Jyothi P Koujaligi handled the sessions on the following topics:

1. Vision Mission – Institute /Department on 22-02-2021
2. Program Outcomes and Programme Specific Outcomes on 23-02-2021

Dr. M. Meenakshi
Professor & Head, EIE
Chief Coordinator, AICTE – MARGDARSHAN SCHEME

26/19-20

Kalpataru Vidya Samsthe (R) Estd : 1961

KALPATARU INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgaum & Recognised by A.I.C.T.E., New Delhi)

N.H. 206, B.H. Road, **TIPTUR - 572 201,** Karnataka, INDIA.

Ph : +91-8134 - 251267, 251938 Fax : +91-8134 - 251939, Mobile : 97400 16919,

www.kittiptur.ac.in, principal@kittiptur.ac.in

Estd: 1986

Ref No. : KIT/principal/2019-20/20

Date 20.02.2020

Attendance Certificate

This to certify that Dr Jyothi P Koujalagi, Prof & Head Dept of E&E Dr AIT Bangalore, has visited our Institution as Coordinator & resource person for One day workshop on "Outcome Based Education and Accreditation " of AICTE-Margadarshan Scheme (SHARE AND MENTOR INSTITUTIONS). She has delivered session talk on "Discussion on SAR Preparation" on Thursday, 20-02-2020.

Principal

Principal
Kalpataru Institute of Technology
TIPTUR - 572 201.

अभियांत्रिकी एवं प्रौद्योगिकी संस्थान
डॉ. राम मनोहर लोहिया अवध विश्वविद्यालय, फैजाबाद (उ.प्र.)
INSTITUTE OF ENGINEERING & TECHNOLOGY
DR. RAM MANOHAR LOHIA AVADH UNIVERSITY FAIZABAD (U.P.)

Invitation letter

To
The Prof Jyoti Koujalagi
HOD, EEE Dept.
Dr. AIT, Bangalore

Dear Sir/Madam,

We are happy to inform you that Department of Electrical & Electronics Engineering, Institute of Engineering &Technology, Dr RML a renowned and reputed institute affiliated to Awadh University, Ayodhya, UP, in collaboration with Dr Ambedkar Institute of Technology, Bengaluru is organizing one week National Workshop on "Electric Vehicle Technology needs and Enviroment", between 16th to 20th Oct 2019.

Furthermore, this is with pleasure that I would like to extend the invitation to the Faculty of Electrical Engineering for keynote speaker / session speaker for the workshop. The brochure for the workshop is attached along with this mail.

The workshop has about 85 participants all over India, to stimulate the desire to collaborate and change the world of Technology and innovation in Electrical Engineering field.

Kindly confirm the session between the dates 16th Oct, 2019 to 18th Oct 2019 Please note: An industrial visit is being arranged on 19th Oct 2019.

With warm regards,
Thanking you

Yours sincerely,
Er. Samrendra Pratap Singh
Assistant Professor
Dept of EEE,
Institute of Engineering &Technology, Ayodhya

RASHTREEYA SIKSHANA SAMITHI TRUST

R. V. COLLEGE OF ENGINEERING

(Autonomous Institution)

Approved by All India Council for Technical Education (AICTE), New Delhi
Affiliated to Visvesvaraya Technological University (VTU), Belagavi, Karnataka
Accredited by National Assessment and Accreditation Council (NAAC)

Ref.:

Date:
13th December 2019

CERTIFICATE OF APPRECIATION

To,
Dr. Jyoti Koujalagi
Professor
Department of Electrical & Electronics Engineering
Dr. AIT, Bengaluru,
Mobile: 74830 05478
Email: jyotipkoujalagi@gmail.com

Dear Sir/Madam,

On behalf of RVCE, we would like to express our heartfelt thanks for accepting our invitation to come to RVCE as an external auditor for our Academic Auditing for the UG and PG programs of EC, TC, IT & EE held on 13th Dec 2019. The audit process was completed successfully. The undersigned would like to extend appreciation for your valuable suggestions and remarks.

Looking forward for your cooperation in future as well.

Thanking you with high regards

Yours Sincerely

PRINCIPAL

PRINCIPAL
R.V. College of Engineering
Bengaluru - 560 059

May 13, 2019

Dr. G.V. Jayaramaiah,
HoD – ECE
Department of Electronics and Communication Engineering
Dr. A.I.T, Bangalore.

Subject: Letter of Appreciation

Dear Sir,

Thank you very much for being the evaluator for our Project Based Learnir projects held on May 13th 2019 at Department of ECE, BMS Institute of Technolo and Management.

It was really a splendid Interaction which exposed students to the field projects.

Looking forward for your cooperation for the promotion of professional educa in future as well.

Yours sincerely,

13/5/19

Dr. Ambika R
'rofessor and Head
'epartment of Electronics and Communication Engineering
MS Institute of Technology and Management
ngalore 560064

JSS Mahavidyapeetha
JSS SCIENCE & TECHNOLOGICAL UNIVERSITY
SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING
JSS Technical Institutions Campus, Mysuru - 570 006, INDIA
An Autonomous institution Under Visvesvaraya Technological University Belagavi
Approved by AICTE and Accredited by National Board of Accreditation
Governed by Grant in Aid rules of Government of Karnataka
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

TO,

26.8.2019

Dr. S.Vasudevamurthy,
Profe ssor,
Department of Electrical and Electronic Engineering,
Dr. Ambedkar Institute of Technology, Bengaluru

Letter of Appreciation

Dear Sir,

It is our honor to thank you for accepting our invitation to be the resource person in the workshop on "Applications in High Voltage Engineering" held on 19th June 2019, organised by department of Electrical Electronics Engineering, JSS Science and Technology University (Formerly SJ College of Engineering), Mysore.

We look forward to your continuous support and encouragement in the future also.

Regards

DrA.D.Srinivasan
Professor and Coordinator of the Workshop
Department of Electrical and Electronic Engineering,
JSS Science and Technology University (Formerly SJ College of Engineering),
Mysore.

Telephones : (off) : 0821-2548285 - 289, Fax : 0821-2548289, email : Admn@sjce.ac.in

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JSS Mahavidyapeetha
JSS SCIENCE & TECHNOLOGICAL UNIVERSITY
SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING
JSS Technical Institutions Campus. Mysuru - 570 006, INDIA
An Autonomous Institution Under Visvesvaraya Technological University Belagavi
Approved by AICTE and Accredited by National Board of Accreditation
Governed by Grant in Aid rules of Government of Karnataka
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

26.8.2019

TO,

Dr. Eranna,
Profe ssor,
Department of Electrical and Electronic Engineering,
Dr. Ambedkar Institute of Technology, Bengaluru

Letter of Appreciation

Dear Sir,

It is our honor to thank you for accepting our invitation to be the resource person in the workshop on "Applications in High Voltage Engineering" held on 19th June 2019, organised by department of Electrical Electronics Engineering, JSS Science and Technology University (Formerly SJ College of Engineering), Mysore.

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Professor and Coordinator of the Workshop
Department of Electrical and Electronic Engineering,
JSS Science and Technology University (Formerly SJ College of Engineering).
Mysore.

Telephones : (off) : 0821-2548285 - 289, Fax : 0821-2548289, email : Admn@sjce.ac.in

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Panchajanya Vidya Peetha Welfare Trust (Regd)

Dr. Ambedkar Institute of Technology

(An Autonomous Institution, Aided by Government of Karnataka
Affiliated to Visvesvaraya Technological University, Belgum & Approved by AICTE New Delhi)

BDA Outer Ring Road, Near Jnana Bharathi Campus, Mallathally, Bengaluru - 560 056

Ref. No. .....

Date : 19/01/2018

Patrons: Trustees P.V.P.Welfare Trust Dr. C Nanjunda Swamy Principal, Dr.AIT **Chief Coordinator :** Dr. Jyoti Koujalagi Professor & HOD, EEE **Coordinators:** Dr.Shankaralingappa Professor EEE, Dr. AIT Sri H V Govinda Raju Associate Professor EEE, Dr. AIT Sri Mukunda Swamy Assistant Professor EEE, Dr. AIT	To Dr. Shankaralingappa C.B. Professor Dr. AIT. Dear Sir Sub: Letter of Appreciation for delivering a Lecture for the FDP on, "***Intelligent Algorithms & Applications***", held from 17th -19th Jan 2018 organized by the Department of EEE, Dr.AIT, Bangalore. We thank you for accepting our invitation & delivering a lecturer on " Genetic Algorithms. ", dated 19/01/2018. Thanking you Yours Sincerely Dr Jyoti Koujalagi

Ph : 23218534, 23211232, 23211505 / 06 / 07, Fax : 080-23217789, E-mail : principal@dr-ait.org. Website : www.dr-ait.org

26/17-18

B.N.M. Institute of Technology

(Approved by AICTE & Affiliated to Visvesvaraya Technological University)
ISO 9001 : 2008 Certified Institution
Administrative Office & Campus : 12th Main Road, Banashankari II Stage, Bengaluru - 560 070
✆ : 91-80-26711780/81/82 Email : bnmitprincipal@gmail.com Website : www.bnmit.org

Vidyaya Amrutham Ashnuthe

Date: 20-01-2018

To,

Dr. Jyoti Koujalagi
Professor & Head,
Department of Electrical & Electronics Engineering,
Dr. Ambedkar Institute of Technology,
BDA Outer ring road, Mallathally,
Bangalore - 560056

Dear Madam,

We thank you for your support in conducting One Week FDP on **"Modern Power Electronic Drives: Design & Future Trends"** as a resource person for technical session and spending your valuable time with us. The FDP attendees had the unique opportunity to learn from the technical talk delivered by you.

We look forward to your continued support and association with BNM Institute of Technology in all our future endeavors.

With warm regards,

Director

Director
BNM Institute of Technology
Post Box No. 7087, 27th Cross, 12th Main
Banashankari 2nd Stage
Bangalore - 560 070

26/16-17

Website: www.cbse.nic.in
Email Id: jeeobserver2016@gmail.com

Phone No.0120-2427773
Fax: 0120-2427772

CENTRAL BOARD OF SECONDARY EDUCATION

(An Autonomous Organisation under the Union Ministry of Human Resource Development Govt. of India)

JOINT ENTRANCE EXAMINATION UNIT/UGC-NET

Block No.-H, Plot No.-149, Sector-63, Noida-201309

No.F.16/JEE/CBSE/EXAM/OBS.2016/E160782/142 **05/03/2016**

CONFIDENTIAL

JYOTHI KAUJALAGI
ASSTT. PROF.
DR. AMBEDKAR INSTITUTE OF TECH.
DEPT. OF E.E. ENGG.
BANGALORE, KARNATAKA
560056

Subject: APPOINTMENT AS OBSERVER FOR JOINT ENTRANCE EXAM (JEE) TO BE HELD ON 3rd APRIL, 2016 (SUNDAY)

Dear Sir/Madam,

The Competent Authority of the Board is pleased to appoint you as Observer for Joint Entrance Examination (Main) {JEE(Main)-2016} to be held on 3rd April 2016 (Sunday) at city **BANGALORE**. You are aware that this is very sensitive examination which requires atmost care in performing duties of an observer who is a public representative and thus need to ensure smooth and fair conduct of this examination. I am sure that you will meet the expectation by adhering to the following instructions.

You are requested to reach the destination at least one day before the day of examination and inform about your arrival to the Coordinator of the concerned city appointed by the CBSE. In cities where coordinators are not appointed by the CBSE, the observers are requested to inform about their arrival to the centre superintendent of the concerned centre. In case the Coordinator/Centre Superintendent is not informed of your arrival, alternate arrangement will be made by the city Coordinator/Centre Superintendent by appointing another person in your place.

The City Coordinator, CBSE Representative(s) and Centre Superintendent(s) of the city/town will conduct the meeting on 2nd April, 2016 at 11.00 a.m. to review and discuss the entire preparation for smooth and fair conduct of examination along with the observer appointed by the CBSE. In this meeting, you will be informed about the centre where you will have to perform the duty of an observer. Without attending this meeting, you will not be able to perform your duty judiciously. Therefore, you are requested to attend this meeting positively. The venue and timings of the meeting may please be ascertained from the Coordinator or from the Centre Superintendent as the case may be.

You are also requested to read carefully the Guidelines for Centre Superintendents, Invigilators and Observers for JEE(Main)-2016 and adhere to in true spirit. You are requested to accompany the confidential material from the delivery place which will be intimated to you in the meeting on 2nd April, 2016 and ensure your presence at the centre during the examination till examination is over. The question paper packets must be opened in your presence as per instructions. The answer scripts must be counted personally and packed in your presence as soon as the examination is over and escort Answer Sheets bundles to the collection centre that will also be informed in the above mentioned meeting. Please send the detailed report on the conduct of JEE(Main)-2016 immediately after the completion of the whole process.

The Honorarium/TA-DA payable to an observer for JEE(Main)-2016 is as under:
Observer for Local Centre –

Paper 1: Rs.3000/-(Rs.2000+Rs.1000 fixed Conveyance)
Paper 1&2: Rs.4000/-(Rs.3000+Rs.1000 fixed Conveyance)

26/16-17

Visvesvaraya Technological University

EXAMINATION SECTION

Belagavi – 590 018.

Dr. H. G. Shekharappa
Registrar (Evaluation)

Phone : (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/825

Date : 03/10/2016

To,

E & E (COMPOSITE) BOARD

SL. No.	Name	College	C/M
1	Dr. M. R. Shiva Kumar	Sampoorna, B'luru	C
2	Dr. B. R. Nataraj	KS School, B'luru	M
3	Prof. A. N. Joshi	GIT, BGM	M
4	Dr. Usha Surendra	NIEIT, M'suru	M
5	Dr. S. N. Shashidhar	RBYM, Bellary	M
6	Dr. H. M. Ravikumar	Nitte Minakshi, B'luru	M
7	Dr. Jyoti Koujalagi	Dr. AIT, B'luru	M
8	Dr. Neelashetty K.	GND, Bidar	M
9	Dr. G. R. Veerendra	AIT, Chickmangaluru	M
10	Dr. Basavaraja Banakara	UBDT, Davangere	M
11	Dr. H. B. Nagesh	BIT, B'luru	M
12	Prof. Basavaraj V. Madiggond	Jain, BGM	M
13	Prof. Anil Kadle	AEC, Bhatkal	M
14	Prof. B. Mallikarjuna	RNSIT, B'luru	M
15	Dr. Kumaraswamy B. G.	SJMIT, Chitradurga	M
16	Dr. Mahesh K.	New Horizen, B'luru	M
17	Prof. Venkatesha K.	BNMIT, B'luru	M

Sir,

Sub: Meeting of Board of Examiners to Scrutinize the Question Papers-reg.

By the Directions of the Vice-Chancellor, a meeting of Board of Examiners is scheduled to Scrutinize the Question Papers for Dec.2016/Jan 2017 Examinations on 25th & 26th October 2016. The Details of the Meeting are as under:

Date	Time	Purpose	Venue
25th & 26th October 2016	10.30 am	Scrutiny of Question Papers	VTU Exam Section, "Jnana Sangam" BELAGAVI - 18

You are requested to make it convenient to attend the meeting. Please note it is mandatory to attend the meeting.

Thanking You,

Yours Sincerely

Registrar (Evaluation)

Copy FWCs to: 1) The Secretary to Vice-Chancellor, VTU Belgaum, for information.
2) The Registrar & Finance Officer, VTU Belgaum, for information.

Note: Bus arrangements are made from the Bus Stand at 9.00 am & from the Railway Station at 9.15 am.

(28)/21.

MVJ COLLEGE OF ENGINEERING
Since 1982

An Autonomous Institution
Permanently Affiliated to VTU, Belagavi, Approved by AICTE, Accredited by NAAC,NBA
Recognized by UGC with 12(f) & 12(B) Status

Office of the Controller of Examinations

coe@mvjce.edu.in 080-42991006/24

Ref. No/MVJCE/Exams/2021-22/78 Date: 14/01/2022

To,

Dr. Jyoti Koujalagi

DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BENGALURU

9972824542

Sir/ Madam,

Sub: Appointment as paper setter for March / April – 2022 Examinations

By the direction of BOE Chairman/Principal, I am pleased to appoint you a subject with details as under. **You are requested to kindly accept the assignment.**

Exam	B.E
Course/ Branch	ELECTRICAL ENGG
Semester	V
Course Code	MVJ19EE53
Course Name	Electrical Machines - 2
To be submitted before:	25/01/2022

INSTRUCTIONS TO PAPER SETTER

- Question papers shall consists of two parts, i.e. Part-A and Part-B.
- Part-A is compulsory and consist of objective type or short answer type questions for 1 or 2 marks each totaling to 20 marks covering the entire syllabus.
- Part B also covers the entire syllabus consisting of five questions having choices and may contain sub-divisions, each carrying 16 marks. Students have to answer five full questions.
- There shall be a maximum of three subdivisions in each question. Please avoid sub divisions, within subdivisions, for the sake of clarity. Wherever applicable, there shall be numerical problem in each question.
- One question must set from each unit
- Duration of exam is 3 hrs.
- Question paper shall be designed as per the Revised Bloom Taxonomy (RBT) levels.
- Questions shall strictly be from the *prescribed* books in the Syllabus, covering the entire syllabus.
- Provide neat sketches as these figures are scanned directly from the manuscript for final printing. Sketches should be drawn in black ink only.

MVJ COLLEGE OF ENGINEERING
Since 1987

An Autonomous Institution
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Recognized by UGC with 12(f) & 12(B) Status

Office of the Controller of Examinations

coe@mvjce.edu.in 080-42991006/24

- Explicitly indicate the figure numbers. For example, identify the figure pertaining to Q 2(c) as Fig.Q2(c) immediately adjacent to it.
- Whenever problems are included, make sure to give all the relevant data from the data handbook, to avoid the issue of Handbooks / Tables / Charts etc.
- However, if special charts, data book are required to be given, please explicitly mention the same in the ' Instruction to the candidate ', at the top of the Question paper.
- Scrupulously scrutinize the question paper for its completeness (Subject code, Subject title, & Clarity), missing data/figure / marks allocated / repetition of entire question or part thereof and out of syllabus questions. Read the question paper meticulously and seal in the appropriate *cover enclosed*, before sending it to the *Institution / College*.
- *Please mail the soft-copy to* qpsee@mvjce.edu.in.
- Kindly take the printout of the same and send to the college address
- For QP related Queries Please contact Prof Kumar R –Exam Section 9902872764

Yours Sincerely

14/11/2022

Controller of Examinations
Controller of Examinations
MVJ College of Engineering
(An Autonomous Institute)
Bangalore - 560 067

Encl:
- Copy of the Syllabus
- Question paper format (word file)
- Remuneration Bill

Q the magnetic flux ~~rotating~~ at the air gap produced by 3φ current rotate at

(a) sub sy (b) sy (c) super sy (d) zero

②

Gmail

Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

Appointment as paper setter - UG/PG Examinations

2 messages

DR. ANANTHA RAMA V <00005536@rvei.edu.in> Fri, Jun 18, 2021 at 4:12 PM
To: jyotipkoujalagi@gmail.com

Dear Sir/Madam,

It is my pleasure to inform you that you are appointed as paper setter for ensuring UG/PG Examinations.
I request you to set the paper for the course as per the attached letter.

With Warm Regards,

Dr G Sadashivappa,
Controller of Examinations,
RV College of Engineering,
Bangalore.

5 attachments

- **Appointment as paper setter - UG/PG Examinations.PDF** 110K
- **Additional document.PDF** 1308K
- **Additional document.PDF** 27K
- **Additional document.PDF** 196K
- **Additional document.PDF** 1308K

Jyoti Koujalagi <jyotipkoujalagi@gmail.com> Sun, Feb 6, 2022 at 1:56 PM
To: Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

[Quoted text hidden]

4 attachments

- **Appointment as paper setter - UG/PG Examinations.PDF** 110K
- **Additional document.PDF** 1308K
- **Additional document.PDF** 27K
- **Additional document.PDF** 196K

IEEE RVCE STUDENT BRANCH

Date: 18/07/2021

To-
Dr. Jyothi Koujalagi
EEE Department
Technology
Bangalore

Dr. Ambedkar Institute of

Through-
Dr SG Srivani
Faculty Advisor
Power and Energy Society, IEEE RVCE

From-
Shreyas Sharma
Chair
Power and Energy Society, IEEE RVCE

Date- 18thJuly2020

Subject- Invitation to be the judge for 'Poster Presentation Competition"

RespectedMa'am,

IEEE RVCE Power and Energy Society is conducting a"Poster Presentation Competition" onthe 22ndof July 2021(Thursday), from 2:00 pm to 4:00pm for PG students of EEE department.We request you to be the judge for the same.

For the official poster we require a photograph of you.

Thanking you
Faithfully

Yours

Dr S.G Srivani
Counselor
PESSociety, IEEE RVCE SB

Shreyas Sharma
1RV18EE066
Chair, PES Society, IEEE RVCE SB

/21

Date: 17.08.2021

From:
Dr Jyoti Koujalagi
Professor
Department of Electrical & Electronics Engg.

To,
The Principal,
Dr. Ambedkar Institute of Technology,
Bengaluru-560056

Through,
The HOD,
Department of Electrical & Electronics Engg.
Dr. Ambedkar Institute of Technology,
Bengaluru-560056

Sir,

SUB: AICTE / VTU ACTIVITY POINTS

This is with reference to the pursuant of AICTE activity points to be earned by the B.E students and to get eligibility for the award of degree.

Students under my mentoring (14 Students) have come forward to install solar street light at Avera Hally, Manchanabelay Jalashaya (Dam) around 25 Kms from our college.

In this regard, I request you to provide college Bus to carry out student's activity on 19.08.21 between 9.00 am to 4.00pm

Please find enclosed the copy of students list and details of the VTU activity points to be earned by the students for your kind perusal and needful.

Thanking you,

Yours faithfully,

Dr Jyoti Koujalagi

Principal c/o c's
Forwarded for needful
17/8/21

Permitted.
17/08/21

PRINCIPAL
Dr. Ambedkar Institute of Technology
Bengaluru-560 056

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ
ಮೈಸೂರು ಅಧಿನಿಯಮ ೧೯೯೪ರ ಅನಿಯಮ, ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ
"ಜ್ಞಾನ ಸಂಗಮ", ಬೆಳಗಾವಿ-೫೯೦೦೧೮, ಕರ್ನಾಟಕ, ಭಾರತ

Visvesvaraya Technological University
(State University of Government of Karnataka Established as per the VTU Act, 1994)
"Jnana Sangama" Belagavi-590018, Karnataka, India
Phone: (0831) 2498100, Fax: (0831) 2405467, Website: vtu.ac.in

Dr. A. S. Deshpande B.E., M.Tech., Ph.D.
Registrar

Phone: (0831) 2498100
Fax: (0831) 2405467

Ref: VTU/BGM/BOS/A9/2020-21 / 447

Date: 27 APR 2021

CIRCULAR

Subject: Activity points for the award of Bachelor's Degree - reg

Reference: 1. AICTE Activity point program (Model Internship Guidelines)
2. VTU/BGM/OS-Aca/Cirs/2020-21/5551, dated: 30.01.2021
2. Hon'ble Vice-Chancellor's Approval Dated:27.04.2021

With reference to the above and pursuant to the AICTE Activity point program, Visvesvaraya Technological University, Belagavi has notified regarding the activity points to be earned by the B.E/B.Tech., students.

Now, because of Covid-19 pandemic, it may not be possible for the students to earn the prescribed AICTE Activity points and get eligibility for the award of degree. Hence as a one-time measure, the number of activity points to be earned by the students is reduced and the same is mentioned in the below table. These reduced points may be proportionality equated to the prescribed activity points-

Sl.no	Student Category	Activity point prescribed by AICTE	Reduced to
1	Day college Regular student admitted to the 4 years Degree program (admitted during 2018-19 and 2019-20)	100	50
2	Student entering 4 years Degree programme through lateral entry (admitted during 2019 -20 and 2020-21)	75	25
3	The student transferred from other Universities to the fifth semester (admitted during 2020-21)	50	25

This is for information and needful in the matted.

Sd/-
REGISTRAR

To,

1. The Principals of all Affiliated/Constituent /Autonomous Engineering Colleges and all Schools of Architecture under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore.

Copy to.

1. To the Hon'ble Vice-Chancellor through the secretary to VC, VTU Belagavi for information
2. The Registrar (Evaluation), VTU Belagavi for information.
3. The Regional Directors (I/c) of all the regional offices of VTU for circulation.
4. The Special Officer CNC VTU Belagavi for uploading on VTU website
5. PS to Registrar VTU Belagavi
6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

REGISTRAR

Gmail

Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

Request to be part of ASLAP Portal as an Expert and/or Reviewer in Emerging Area

2 messages

IT Consultant SLA <itconsultantsla@aicte-india.org> Wed, Jun 23, 2021 at 9:58 AM
To: Col B Venkat <director.fdc@aicte-india.org>
Bcc: jyotipkoujalagi@gmail.com

Dear Sir/Madam,

Greetings from the All India Council for Technical education!!

AICTE would be extremely grateful if we receive your questions and appreciates your valuable contribution towards the preparation of the question bank for Emerging Areas as part of the Student Learning Assessment (SLA) Project and give your part of the contribution in this National level initiative to understand the Level and Gains of students studying in Technical Institutions of India.

We had launched a portal to automate the submission of Questions for the Student Learning Assessment Project. This portal will be the rich and smart source of the questionnaire for this project.

We request you to contribute your questions to this Project by submitting the questions using this portal.
You are free to forward this message to your faculty members for further propagation of this national-level work initiated by AICTE.

Guidelines

- Register here --> https://aslap.aicte-india.org/register as EMERGING AREA
- Verify Registered Email id
- Login Here --> https://aslap.aicte-india.org/
- You should have only one Login and upload ALL the questions using that one only.
- Minimum 30 questions have to be submitted and no maximum limit for question submission.
- Maintain questions set L1+L2=70% rest L3 as per the Bloom's taxonomy.
- Please follow the attached User Manual to Upload and Submit the questions
- Bank Mandate Form Sample
- The tentative deadline is 25/**06/2021(It may be extended)**

Please reach out to dataanalystsla@aicte-india.org and itconsultantsla@aicte-india.org for any concerns or queries.
Emerging Areas Topics:

1. Artificial Intelligence / Machine Learning
2. Augmented Reality (AR)/ Virtual Reality (VR)
3. Blockchain
4. Cyber Security
5. Data Science
6. Internet of Things (IoT)
7. Quantum Computing
8. 3D Printing and Design
9. Robotics

Kindly update the question by selecting 3rd and 4th Year.
Please fill the google form below.

https://forms.gle/cWAWaQtAyahB2Yy88

28/21

Ministry of Education
Ministry of Information and Broadcasting
Ministry of MSME

सत्यमेव जयते

Ministry of Women and Child Development
Ministry of Commerce & Industry
Ministry of Textiles

Ministry of Education's
INNOVATION CELL
(GOVERNMENT OF INDIA)

Toycathon 2021

CERTIFICATE

Primary Evaluator

This Certificate is awarded to

---

for exceptional contribution as a Primary Evaluator in

'Toycathon, 2021'.

Prof. Anil Sahasrabudhe
Chairman
AICTE

Dr. Abhay Jere
Chief Innovation Officer
Ministry of Education Innovation Cell

Dr. Mohit Gambhir
Innovation Director
Ministry of Education Innovation Cell

Gmail

Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

Selected as a Reviewer for the International Journal of Research and Innovation in Social Science (IJRISS)

3 messages

RSIS International <reviewer@rsisinternational.org> Fri, Apr 9, 2021 at 3:14 PM
To: "jyothi.k.ee@drait.edu.in" <jyotipkoujalagi@gmail.com>
Cc: Editorialoffice <editorialoffice@rsisinternational.org>

Dear Dr. JYOTI KOUJALAGI,

Greetings from Research and Scientific Innovation Society!!

As per your application, you are selected as a Reviewer in the International Journal of Research and Innovation in Social Science (IJRISS).

Peer review and reviewers – are at the heart of the academic publishing process. Being a Reviewer of the journal is truly productive, pleasant and in fact prestigious which helps in add-on to the academic world through the ways and guidelines given by experts in the relevant fields. You will be entitled following benefits while working with us as a Reviewer of the journal.

Benefits

- You can add your role as a Reviewer for the International Journal of Research and Innovation in Social Science (IJRISS) *through Art* to their CV.
- This work helps to add in and provides a better way to create your identity as a well-known expert in your field and may lead to increased invitations to speak at conferences or demand for invited research of your specialized area.
- You will be among the contributors who will shape and decide the urgent ways as required with changing societal needs.
- You will come across the latest research before everyone else and gives you a position of leadership in your research community.
- Reviewers could enjoy discounts on Article Processing Charges if they publish articles/refer in RSIS Journals.

Roles & Responsibility

- Providing written, unbiased, constructive feedback in a timely manner.
- Notifying the Editorial Office immediately if unable to review in a timely manner and, if able, providing the names of alternative reviewers.
- Refraining from direct author contact.
- Careful examination of purposes and objectives of the work carried out.
- Correctness of the conclusions and recommendations along with up-to-date references
- Coverage areas of the manuscript in relevance with the scopes of journals

The acceptance of following terms and conditions confirms your appointment as a Reviewer.

- Your appointment is initially for one year.
- You are expected to carefully observe general policies which may change from time to time based on expansion plans for the improvement in the quality of the journal system.
- You agree to display your name on the website of the site.

Termination

It is expected that you will complete the term as stated. This agreement may be terminated at any time based on following conditions.

- Lack of mutual understanding on common aspects as per the policies of the journal.
- Repetitive unsatisfactory performance of the assigned work
- No response on the two consecutive review papers shared for reviewing

Duration of Membership will be 1 Year starting from February 2021 to January 2022, later on members need to resubmit application form and committee will decide for further process.

28/19

Gmail

Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

Regarding BOS

2 messages

dilip yadav <dilipyadav@rmlau.ac.in> Fri, Apr 24, 2020 at 10:51 PM
To: jyotipkoujalagi@gmail.com

Dear Madam,

As per Earlier Conversation/Email about the BOS meeting ,

Date of Board of Studies:25/04/2020

Time:1:30 pm

Venue :IET,Dr.RLAU Ayodhya through **Zoom App** (or alternatively in Whats App,Email,Voice Call, any mode in which you feel free to connect)

Zoom User ID:85173557909

Password: 223344

Link:https://us02web.zoom.us/j/85173557909?pwd=aTFqbklYVFQ5a0s0N1A3WFhWaUpPUT09

I am attaching agenda of the meeting.B.Tech Ordinance ,Syllabus of B.Tech 2nd year ,B.tech 3rd Year and B.Tech 4th Year and also the External Examiner List Along with this email.So Please find the attachment.

With warm regards;
Er. Dilip Kumar .
H.O.D
Electrical Engineering
Institute of Engineering & Technology
Dr. Rammanohar Lohia Avadh University
Ayodhya, U.P. 224001
+918574390711

7 attachments

- **Agenda BoS 2020 EE.docx** 397K
- **B. Tech. Ordinance_2020.pdf** 475K
- **B.Tech. 2nd Year Electrical Engg.docx** 220K
- **B.Tech. 3rd Year Electrical Engg..doc** 358K
- **B.Tech. 4th Year Electrical Engg.docx** 218K
- **B.Tech.1st Year Electrical Engg Syllabus.docx** 32K
- **Pool List of Examiners for RegularCarry Over Examination.docx** 39K

Jyoti Koujalagi <jyotipkoujalagi@gmail.com> Mon, Feb 7, 2022 at 11:36 AM
To: Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

28/18

Gmail

Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

Invitation for Board of Studies(BOS) Meeting on 27-07-2019

3 messages

Sanjay Chauhan <ee.chauhan@gmail.com> Wed, Jul 24, 2019 at 5:23 PM
To: ksv211@rediffmail.com, jkdwivedi.hbti@gmail.com, ksahay.iitd@gmail.com, rishihbti@gmail.com, sumangala_bv@yahoo.com, jyotipkoujalagi@gmail.com

Respected Ma'am/Sir,

This is my pleasure to invite you in the BOS meeting of our Department for the session 2019-20 which is scheduled on 27-07-2019 at 11:00 am(office order is attached). I am also sending the soft copy of the proposed syllabus of UG program for your valuable suggestions and looking forward for your gracious presence on the day.

Warm Regards
Sanjay Chauhan

Sanjay Chauhan
Assistant Professor & Head
EE Dept. IET
Dr. RML Awadh University Faizabad, Ayodhya (U.P.) INDIA
Mob. 9454001234

2 attachments

Office_Order.pdf
526K

CBCS_2nd_Year_Syllabus_IET_Ayodhya_2019.pdf
163K

Jyoti Koujalagi <jyotipkoujalagi@gmail.com> We
To: Sanjay Chauhan <ee.chauhan@gmail.com>

Thanks for the invitation
Very busy, sorry will not be able to attend the meeting.
I will surely pass my suggestions shortly

Best regards
[Quoted text hidden]

Jyoti Koujalagi <jyotipkoujalagi@gmail.com> Mon, Feb 7, 2022 at 11:28 AM
To: Jyoti Koujalagi <jyotipkoujalagi@gmail.com>

[Quoted text hidden]

28/19

Dr Ambedkar Institute of Technology

(An Autonomous Institution affiliated to VTU, Belgaum)

Dr.AIT/Dean/ 158 /2019-20 26.10.2019

CIRCULAR

The **Academic Audit Committee** shall monitor all the academic activities related to the conduction of classes, and evaluation of CIE for all UG and PG courses. The committee consists of the following faculty members and the departments to be monitored/audited are as mentioned against their updated names in the table shown:

Sl No	Members	Updated Members	Departments to be monitored
1	a) Dr.S.Ramesh,ECE b) Sri. A.S. Jayram, ME	a) Dr.Jyothi Koujalagi,EE b) Sri. A.S. Jayram, ME	Civil (PG and UG)

The above updated committee members are hereby informed to submit the report to the Dean (Academic) as per the format enclosed by 30.10.2019 **positively.** Please take the photocopy of the format if required.

Dean (Academic)

Dean (Academic)
Dr. Ambedkar Institute of Technology
Bangalore - 560 056.

Copy to: 1. Principal W/cs for information.
2. Concerned HODs and concerned members.

28/18

SRI SIDDHARTHA INSTITUTE OF TECHNOLOGY, TUMKUR

(A Constituent College of Sri Siddhartha Academy of Higher Education, Tumkur)

DEPARTMENT OF DEAN (EXAMINATION)

REF:

Date: 01.03.2018

STRICTLY CONFIDENTIAL

PROF.

Prof. Jyothi. P.K
Professor, Dept. of Electrical &
Electronics Engg.,
Dr. Ambedkar Institute of Tech,
2 Stage, Nagarbhavi, Bengaluru,
Karnataka 560056.
Mb. 9972824542

Warm wishes from Sri Siddhartha Institute of Technology, Tumkur

We are pleased to include your name in our examination panel for the forthcoming examinations. We appreciate your valuable contribution to the field of engineering education. We are sure your expertise and experience will help to build a strong academic ambience and quality education.

Our semester end examination is scheduled in the month of MAY 2018. In this regard, we request you kindly to help us by preparing the question paper for the end term examination.

We hope you will accept our request and do needful. Necessary informati[redacted] with this letter. Please send the question paper on or before **20th March 2018**. *Q*[redacted] *format can be downloaded from our website:* **www.ssit.edu.in**

Thanking you in advance for your kind time.

Dr. Karunakara K
(Dean Examination)

28/17-18

Dr.AIT/EXAM/2017-18 1194 18.07.2017

The Registrar (Evaluation),
Visvesvaraya Technological University,
Examination Section,
Jnana Sangama,
BELGAVI-590 018.

Sir,

Sub: - Alternative arrangement for the duties of Chief Superintendent for July-2017 UG/PG VTU examinations.

With reference to the above cited subject Dr. Jyothi koujalagi, Associate Professor & Head Department of Electrical Engineering is authorized to look after the duties of the Chief Superintendent from 19.07.2017 to till end of the examination of the UG/PG VTU examinations.

Thanking you,

Yours faithfully,

PRINCIPAL

18/07/17

Copy to:-
Dr. Jyothi koujalagi
Associate Professor & Head,
Department of Electrical Engineering.

Copy to:
1. EST Section
2. PA to Principal for information
3. Examination Section.

28/

Visvesvaraya Technological University

Jnana Sangama, Belagavi-590 018, Karnataka State

Attendance Certificate

Date: 29/05/2017

This is to certify that Dr. Jyoti Koujalagi, Professor and Head, Dept. of EEE Dr. AIT, Bengaluru-56 has attended LIC Visit as one of the members on 27th to 29th May 2017 at Bengaluru.

Dr. C N Chandrappa
Chairman
LIC, VTU

Visvesvaraya Technological University

Jnana Sangama", Belagavi-590 018, Karnataka State

Dr.H.N. Jagannatha Reddy B.E, M.E., Ph.D.
Registrar

Phone: (0831) 2498100
Fax: (0831) 2405467

Ref. VTU/ACA/2016-17/A-4/ 1186

Date: 23 MAY 2017

To,

1	**Dr. C N Chandrappa** Prof.& Head, Dept. of AU, Acharya IT, Bengaluru **M:9845057634.**	**Chairman**	2	**Dr. Suresh Mane** ME, Prin. Girijabai Sail IT, Karwar **M: 9448433574**	**Member**
3	**Dr. Laxmipathaiah H K** Professor, Dept, of Civil Engg, BIT, Bengaluru **M:9341183022**	**Member**	4	**Dr. Jyoti Koujalgi** Professor, Dept, of E&E Engg, Dr.AIT, Bengaluru **M:997282454**	**Member**
5	**Dr. Vidhya Vivekananda** Professor, Dept, of CS/IS, Vivekananda IT, Bengaluru **M:9986011084**	**Member**	6	**Prof. R. Nagaraj** Dept, of MBA, PESITM, Shivamogga **M:9449170795**	**Member**
7	**Dr.Sandya Anvekar** Professor, Dept, of VTU PG Centre, Muddenhalli. **M:9844046719**	**Member Convener**			

Sir,

Sub: LIC Committee for the academic year 2017-2018-reg.

By the directions of the Hon'ble Vice-Chancellor, I have the honour to invite you to be on the Local Inquiry Committee for inspecting the following Engineering Colleges for granting **Permanent / Continuation / Extension of affiliation** for the academic year 2017-18, and shall submit a report through the Member Convener.

1. Sri Krishna Institute of Technology, Benglauru
2. Sapthagiri College of Engineering, Bengaluru **(To collect the balance VTU Consortium fee of Rs.3,600=00 and Rs.8,52,800=00)**
3. East West College of Engineering, Benglauru **(To collect the balance Affiliation fee of Rs.75,000=00)**
4. M.S.Engineering College, Bengaluru **(To collect the balance Affiliation fee of Rs.6,30,000=00)**

I request you kindly to accept the nomination on the committee.

Thanking you,

Yours faithfully,

REGISTRAR

28/02-18

Dr. Ambedkar Institute Of Technology
(Autonomous Institution, Affiliated to V T U, Belgavi)

19.02.18

CIRCULAR

As a part of Industry- Institution Interaction and bringing students closer to innovation, **Festo Mobile Expotainer** facilitates innovation on wheels with different parts and equipment used in the industries.

The Festo Mobile Expotainer features a variety of dynamic and static displays which shows fast and comprehensive overview of the latest products, solutions, applications with individual explanation of all functions used in Industrial Automation.

In this regard, the company have frozen the schedule to exhibit at our campus on **20th Feb, 2018**, between **09.30am to 04.30 pm**.

All HODs are requested to communicate to students and faculties to get benefitted from customized presentation by their engineers on site, discussions, thus adding value of industry component in academics.

HOD, EEE

HOD
Electrical & Electronics Engg
Dr. Ambedkar Institute of Technology
Bangalore-560 056

Principal

PRINCIPAL
Dr. Ambedkar Institute of Technology
Bangalore - 560 056.

28/17-18

Dr. Ambedkar Institute of Technology
TEQIP-III

AIT/TEQIP-III/ 329 /2018-19 20/09/2018

INTIMATION LETTER

The following faculty members have been permitted to visit Institute of Engineering and Technology (RML Awadh University Faizabad, Uttar Pradesh) to teach Engineering students of various UG subjects at from 24th Sept 2018 to 26th Sept' 2018. They are hereby informed to make necessary arrangements to attend the above.

Sl. No.	Name	Department	Designation
1	Dr. Jyothi Koujalagi	EEE	Prof. & Head
2	T B Dayananda	EEE	Associate Professor
3	Dr, M. V. Mandi	ECE	Professor
4	Dr. S. Ramesh	ECE	Professor
5	Dr. Shivaputra	ECE	Assistant Professor
6	Dr. K. R. Shylaja	CSE	Associate Professor
7	Mrs. Vinutha M.S.	CSE	Assistant Professor
8	Mrs. Arathi P	CSE	Assistant Professor
9	Vijayalaxmi R Patil	ISE	Assistant Professor
10	Dr. Gangadhara Shetty	ME	Professor
11	Dr. Mahadeva Swamy M	ME	Assistant Professor
12	Mr. Shivappa	ME	Assistant Professor

PRINCIPAL
Principal
Dr. Ambedkar Institute of Technology
TEQIP-III
Bengaluru - 560 056.

वर्ल्ड बैंक प्रोजेक्ट पर मंथन शुरू

अवध विश्वविद्यालय में पहुंची आचार्यों की टीम। फोटो : एसएनबी

■ सहारा न्यूज ब्यूरो

फैजाबाद।

डा. राममनोहर लोहिया अवध विश्वविद्यालय के इंजीनियरिंग विभाग में वर्ल्ड बैंक प्रोजेक्ट के अंतर्गत तीन दिवसीय कार्यक्रम का उद्घाटन कुलपति प्रो. मनोज दीक्षित ने किया। टेक्यूप-3 द्वारा निर्धारित मेंटर संस्थान के रूप में डा. अम्बेडकर इंस्टीयूट आफ टेक्नोलोजी बैंगलोर के विभिन्न विभागों से कुल 12 आचार्य व उपाचार्य आईटी संस्थान में आए हुए हैं।

इलेक्ट्रानिक्स विभाग में डा. शिवपुत्रा एवं डा. एस. रमेश ने छात्रों के साथ विभिन्न अलग-अलग सेशन लिया। इसमे संस्थान के इंजीनियर जैनेंद्र प्रताप ने सहयोग किया। इन्फारमेशन टेक्नोलोजी में श्रीमती आरती ने विभाग के छात्रों के साथ विभिन्न अलग-अलग सेशन लिया। इसमें संस्थान के इंजीनियर समृद्धि सिंह ने सहयोग किया। कम्प्यूटर साइंस में विनुथा एमएस ने छात्रों के साथ विभिन्न अलग-अलग सेशन लिया। इसमें संस्थान के इंजीनियर पीयूष राय ने सहयोग किया। इलेक्ट्रिकल विभाग में ज्योति टीबी दयानंदा ने छात्रों के साथ विभिन्न अलग-अलग सेशन लिया। इसमें संस्थान के इंजीनियर समरेंद्र प्रताप एवं दिलीप ने सहयोग किया। मैकेनिकल इंजीनियरिंग में डा. गंगाधर शेट्टी, डा. महादेवा स्वामी एवं शिवअप्पा ने छात्रों के साथ विभिन्न अलग-अलग सेशन लिया। इसमें संस्थान के इंजीनियर आशुतोष मिश्रा सहयोग किया। विभिन्न विभाग के छात्र/छात्राओं को विभिन्न तकनीकी सेशन का लाभ मिल रहा है।

इस ट्यूनिंग प्रोग्राम के को-आर्डिनेटर इंजीनियर मनीषा यादव एवं अवधेश यादव ने कार्यक्रम की रूपरेखा में विस्तार से प्रकाश डाला। इस अवसर में सुप्रिया, डा. प्रियंका श्रीवास्तव, परिमल, पारितोष, विनीत सिंह, डा. ब्रजेश, डे. रवि, चंदन, अमितेश, रमेश मिश्र, साम्भवी, रजनी, अनुराग आदि शिक्षक उपस्थित रहे।

Visvesvaraya Technological University

Belagavi – 590 018.

Dr. Satish Annigeri Ph.D.
Registrar (Evaluation)

Phone : (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/ 1944

Date: 24 AUG 2017

To,

Dr. Jyothi Koujalgi
Dr. AIT, B'luru
Member,
BOE in E & E Board,
VTU, Belagavi.

Dear Sir,

The whole range of activities guiding, planning and execution of the conduction of examinations have been successfully completed with the whole hearted support of all of you during the academic year 2016 - 2017.

I would like to personally thank you for your active participation and excellent cooperation in completing this year-long assignment.

The University looks forward to your continued support in all its activities.

With warm regards,

Yours faithfully

Registrar (Evaluation)

28/16-17

Visvesvaraya Technological University

EXAMINATION SECTION

Belagavi – 590 018.

Dr. H. G. Shekharappa
Registrar (Evaluation)

Phone : (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/ 665

Date: 10/08/2016

To,

E&E (COMPOSITE) BOARD

SL. No.	Name	College	C/M
1	Dr.M.R.Shivakumar	Sampoorna,B'luru	C
2	Dr.B.R.Nataraj	KS School,B'luru	M
3	Prof.A.N.Joshi	GIT,BGM	M
4	Dr.Usha Surendra	NIEIT,M'suru	M
5	Dr.S.N.Shashidhar	RBYM,Bellary	M
6	Dr.H.M.Ravikumar	Nitte Minakshi,B'luru	M
7	Dr.Jyothi Koujalgi	Dr.AIT,B'luru	M
8	Dr.Surendra S.	JNNCE,Shivamoga	M
9	Dr.Neela Shetty	GND,Bidar	M
10	Dr.G.R.Veerendra	AIT,Chickmangaluru	M
11	Dr.Basavaraja Banakara	UBDT,Davangere	M
12	Dr.H.B.Nagesh	BIT,B'luru	M
13	Prof.Basavaraj V.Maddigond	Jain,BGM	M
14	Prof.Anil Kadle	AEC,Bhatkal	M
15	Prof. Mallikarjuna	RNSIT,B'luru	M
16	Dr.Kumaraswamy	SJMIT,Chitradurga	M
17	Dr.Mahesh	New Horizen,B'luru	M
18	Prof.Venkatesh K.	BNMIT,B'luru	M

Sir,

Sub: Meeting of Board of Examiners – reg.

Meeting of Board of Examiners is scheduled as detailed below:

Date	Time	Venue
3rd September 2016	**10.30 a.m.**	**VTU Exam Section, "Jnana Sangam" BELAGAVI - 18**

Kindly make it convenient to attend the meeting without fail. Your active participation in the meeting will help us to enhance the quality of examination process.

Thanking you,

Yours faithfully

Registrar (Evaluation)

Copy FWCs to: 1) The Secretary to Vice-Chancellor, VTU Belgaum, for information.
2) The Registrar & Finance Officer, VTU Belgaum, for information.

Note: Bus arrangements are made from the Bus Stand at 9.00 am & from the Railway Station at 9.15 am.

Visvesvaraya Technological University
EXAMINATION SECTION
Belagavi – 590 018.

Dr. Satish Annigeri Ph.D.
Registrar (Evaluation)

Phone: (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/1437

Date : 02-05-2017

To,

E & E (COMPOSITE) BOARD

Sl. No.	Name of the Chairman/Members	College	Coordinator for
1.	Dr.M.R.Shivakumar	SRSIT, B'luru	Bengaluru Region
2.	Dr.Mahesh K.	New Horizen,B'luru	
3.	Dr.Jyothi Koujalgi	Dr.AIT,B'luru	
4.	Prof.Venkatesh K.	BNMIT,B'luru	
5.	Prof.Basavaraj V.Maddigond	Jain,BGM	Belagavi Region
6.	Prof.Anil Kadle	AEC,Bhatkal	
7.	Dr.S.N.Shashidhar	RBYM,Bellary	Kalaburagia Region
8.	Dr.G.R.Veerendra	AIT,Chickmangaluru	Mysuru Region
9.	Dr.Basavaraja Banakara	UBDT,Davangere	
10.	Dr.Kumaraswamy B.G.	SJMIT,Chitradurga	

Sir,

Sub: Allotment of Examiners for Practical Examinations – reg.

You have been appointed as coordinator for the allotment of examiners for practical examinations of the **Crash Semester Course (May 2017) and June-July 2017** for the colleges coming under your region.

The coordinator has to fix a date to meet at the respective regional office. The regional office will provide all support like list of examiners, stationery, dispatch of allotment letters etc.

Further, you may co-opt some of your colleagues to assist you, with the prior approval of the undersigned (One member could be co-opted for every 10 colleges).

You may contact the Special Officer of respective regional office for further queries.

IMPORTANT: Allotment of examiners for practical examinations of **M.Tech. Programmes** should be done by the **Chairman of the respective Board of Examiners only**.

Thanking you,

Yours faithfully,

2/5/17

Registrar (Evaluation)

Copy to: The Special Officer of VTU Regional Offices, for information and needful.

Visvesvaraya Technological University
Jnana Sangama, Belgaum – 590 018.

Dr. H.G. Shekharappa
Registrar (Evaluation)

Phone:(0831)2498136
Fax : (0831)2405461

Ref.No / VTU / Exam/ Accts /2016-17/

Date : 26/10/16

ATTENDANCE CERTIFICATE

This is certify that, Prof. Dr. Jyoti Kowjalagi

Dr. AIT. Blore has attended the BOE Meeting at

VTU Examination Section, Belgaum on 25/10/16 & 26/10/16

Visvesvaraya Technological University BELGAUM

REGISTRAR (EVALUATION)

Visvesvaraya Technological University
EXAMINATION SECTION
Belagavi – 590 018.

Dr. Satish Annigeri Ph.D.
Registrar (Evaluation)

Phone : (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/1341 Date : 01-04-2017

To,
E & E (COMPOSITE) BOARD

Sl. No.	Name	College	C/M
1	Dr. M. R. Shivakumar	SRSIT, B'luru	C
2	Dr. B. R. Nataraj	KS School, B'luru	M
3	Prof. A. N. Joshi	GIT, BGM	M
4	Dr. Usha Surendra	NIEIT, M'suru	M
5	Dr. S. N. Shashidhar	RBYM, Bellary	M
6	Dr. H. M. Ravikumar	Nitte Minakshi, B'luru	M
7	Dr. Jyoti Koujalagi	Dr. AIT, B'luru	M
8	Dr. Neelashetty K.	GND, Bidar	M
9	Dr. G. R. Veerendra	AIT, Chickmangaluru	M
10	Dr. Basavaraja Banakara	UBDT, Davangere	M
11	Dr. H. B. Nagesh	BIT, B'luru	M
12	Prof. Basavaraj V. Madiggond	Jain, BGM	M
13	Prof. Anil Kadle	AEC, Bhatkal	M
14	Prof. B. Mallikarjuna	RNSIT, B'luru	M
15	Dr. Kumaraswamy B. G.	SJMIT, Chitradurga	M
16	Dr. Mahesh K.	New Horizen, B'luru	M
17	Prof. Venkatesha K.	BNMIT, B'luru	M

Sir,

Sub: Meeting of Board of Examiners to Scrutinize the Question Papers-reg.

By the Directions of the Vice-Chancellor, a meeting of Board of Examiners is scheduled to Scrutinize the Question Papers for June/July 2017 Examinations on 19th & 20th April 2017. The Details of the Meeting are as under:

Date	Time	Purpose	Venue
19th & 20th April 2017	**10.30 am**	**Scrutiny of Question Papers**	**VTU Exam Section, "Jnana Sangam" BELAGAVI - 18**

You are requested to make it convenient to attend the meeting. Please note it is mandatory to attend the meeting.

Thanking You,

Yours Sincerely

3/4/17

Registrar (Evaluation)

Copy FWCs to: 1) The Secretary to Vice-Chancellor, VTU Belagavi, for information.
2) The Registrar & Finance Officer, VTU Belagavi, for information.

Note: Bus arrangement is made from the Bus Stand at 9.00 am & from the Railway Station at 9.15 am.

Visvesvaraya Technological University
EXAMINATION SECTION
Belagavi – 590 018.

Dr. Satish Annigeri Ph.D.
Registrar (Evaluation)

Phone: (0831) 2498136
Fax : (0831) 2405464

Ref. No./VTU/Exam/BOE/2016-17/1437

Date : 02-05-2017

To,

E & E (COMPOSITE) BOARD

Sl. No.	Name of the Chairman/Members	College	Coordinator for
1.	Dr.M.R.Shivakumar	SRSIT, B'luru	Bengaluru Region
2.	Dr.Mahesh K.	New Horizen,B'luru	
3.	Dr.Jyothi Koujalgi	Dr.AIT,B'luru	
4.	Prof.Venkatesh K.	BNMIT,B'luru	
5.	Prof.Basavaraj V.Maddigond	Jain,BGM	Belagavi Region
6.	Prof.Anil Kadle	AEC,Bhatkal	
7.	Dr.S.N.Shashidhar	RBYM,Bellary	Kalaburagia Region
8.	Dr.G.R.Veerendra	AIT,Chickmangaluru	Mysuru Region
9.	Dr.Basavaraja Banakara	UBDT,Davangere	
10.	Dr.Kumaraswamy B.G.	SJMIT,Chitradurga	

Sir,

Sub: Allotment of Examiners for Practical Examinations – reg.

You have been appointed as coordinator for the allotment of examiners for practical examinations of the **Crash Semester Course (May 2017) and June-July 2017** for the colleges coming under your region.

The coordinator has to fix a date to meet at the respective regional office. The regional office will provide all support like list of examiners, stationery, dispatch of allotment letters etc.

Further, you may co-opt some of your colleagues to assist you, with the prior approval of the undersigned (One member could be co-opted for every 10 colleges).

You may contact the Special Officer of respective regional office for further queries.

IMPORTANT: Allotment of examiners for practical examinations of **M.Tech. Programmes** should be done by the **Chairman of the respective Board of Examiners only**.

Thanking you,

Yours faithfully,

2/5/17

Registrar (Evaluation)

Copy to: The Special Officer of VTU Regional Offices, for information and needful.

28/16-17

Eco Fest

Certificate of Appreciation

This is to certify that JYOTHI. P. KOUJALAGI

was participated in Eco Fest on 30th January 2016

held at Vokkaligara Sangha School Ground, Kottigepalya, Nagarabhavi, Bengaluru - 560 091.

Chairman	Joint Commissioner	General Secretary	Chairman
KSPCB	BBMP-RR NAGAR	USIRU FOUNDATION	SRISHTI INTERNATIONAL

Sree Siddaganga Education Society (R.)
Siddaganga Institute of Technology
(An Autonomous Institute Affiliated to Visvesvaraya Technological University, Approved by AICTE, & ISO 9001: 2008 Certified)
B.H. Road, Tumkur - 572 103, Karnataka, INDIA.
Ph.: Direct - 0091-816 - 2282696, 2214001, (Off.) +91-816- 2214000, Fax : +91-816-2282994
E-mail : principal@sit.ac.in, sksittum@yahoo.com Phone/Fax : (Res.) +91-816-2280508, Web : www.sit.ac.in

Ref. S.I.T. No. : SIT/IQAC/2021-22/1

Date : 01-10-2021.

To,
Dr. Jayaramaiah G.V.
Professor & Head
Dept. of EEE, Dr.AIT,
Bangalore

Dear Sir,

Sub: Invitation to carry out Academic Audit.

I, hereby request you to kindly accept our invitation and conduct the academic audit as per the schedule and help us to maintain high academic standards in SIT, Tumakuru.

Academic Audit for 2020-21 of Dept. of **Electrical & Electronics Engg.** is scheduled on **Thu 7th Oct. 2021 9.30 AM.**

Thanking you,

Yours sincerely,

Girish SV
01/10/2021

Principal & Chairman, IQAC

Mob: +91 9448925479
E-mail: tsprasanna@yahoo.com

BANGALORE UNIVERSITY

Prof. T.S. Prasanna Ph.D.

Professor & Chairman
Department of Studies in Electrical Engineering,
University Visvesvaraya College of Engineering,
K.R. Circle, Bengaluru-560 001.

Date: 22-11-2021

To,

Dr, Eranna
Professor
Department of Electrical Engg.,
Dr. AIT, Bengaluru-56.

Sir,

Sub: Appointment of Temporary Guest Faculty for the year 2021-22 for UG & PG course-reg

I am happy to inform you that, you are selected as Subject Expert member for the selection of Temporary Guest Faculty in the Department of Electrical Engineering, UVCE.

The interview will commence from 2 PM on 23rd November 2021. Please accept and make it convenient to attend for selection of candidates.

Thanking you,

Yours faithfully,

22/11/21

PROFESSOR & CHAIRMAN

CHAIRMAN
D.O.S. in ELECTRICAL ENGINEERING
U.V.C.E., K.R. Circle,
BANGALORE - 560 001

SRI SIDDHARTHA ACADEMY OF HIGHER EDUCATION

("Deemed to be University u/s 3 of the UGC Act, 1956")

Accredited 'A' Grade by NAAC

Agalakote, B.H.Road, Tumkur – 572 107.KARNATAKA, INDIA.

Dr. G.M. Shivakumarappa
Controller of Examinations

Ph. 0816-2275514
E-Mail : coe@sahe.in

No. SSAHE/Ph.D/46/2021

Date: 16/04/2021

ATTENDANCE CERTIFICATE

This is to certify that Dr.Eranna, Professor, Dept.of EEE, Dr.Ambedkar Institute of Technology, Bangalore, Karnataka has attended the final viva-voce examination of Ph.D Scholar Mr.K L Rathnakar as External Examiner on 16.04.2021 at Sri Siddhartha Academy of Higher Education, Tumkur, Karnataka.

Place: Tumkur
Date: 16.04.2021

Controller of Examinations

CONTROLLER OF EXAMINATIONS
Sri Siddhartha Academy of Higher Education
TUMKUR - 572 107, KARNATAKA

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RV COLLEGE OF ENGINEERING®
BENGALURU-560059

Dissertation Evaluation Marks Sheet (M.Tech/MCA)

Date: 04-08-2021

Name of the Candidate:	Shwetha A	USN:	1RV19EPE16
Title of Dissertation:	DEVELOPMENT AND TESTING OF SPEED CONTROL ALGORITHM OF PMSM FOR COMPRESSOR LOAD		
Dissertation submitted: (✓ appropriately)	First time ✓	Second time	

Report on the Evaluation of Dissertation Work

SATISFACTORY & RECOMMENDED for approval in partial fulfillment for the award of the PG degree.(✓ appropriately)	YES

Distribution of Marks

Sl.No.	Particulars	Max Marks	Marks Obtained
1.	Relevance of the Subject in Present Context	10	09
2.	Originality	05	05
3.	Literature Survey	10	09
4.	Problem Formulation	10	09
5.	Experimental Observation/Theoretical Modelling	10	09
6.	Results-Presentation & Discussion	20	18
7.	Conclusion & Scope of Future Work	05	05
8.	Overall Presentation of the Thesis	25	23
9.	Outcome of the Dissertation Results in an Article	05	05
	Total Marks (in figure)	100	92
	Total Marks (in words)	Ninety two only	

Salient Features of the Work in brief: The Candidate Shwetha A bearing USN: **1RV19EPE16,** has selected a topic in the relevant field and has done a good literature survey in field. The speed control algorithm of PMSM using EKF technique is developed for rotor position and speed estimation in the present work. The simulation results of sensored and EKF based sensorless control technique is verified for operating speeds ranging from 100rpm to 1800rpm and loaded torque of 59Nm and 118Nm. The error between the actual and estimated speed for 59Nm and 118Nm load is less than 3% and 5% respectively. All the simulation results are validated for steady state conditions. The simulation results depict that the good accuracy of estimated speed for proposed EKF based sensorless control technique. The work carried out by the candidate is satisfactory for the award of the M.Tech degree.

I recommend the Candidate for her oral presentation of the work before the Examiners for further Process.

Dr.Eranna	Associate Professor	Dr.AIT, Bengaluru	04/08/2021
Name of the Examiner	**Designation**	**Organization**	**Signature of Examiner with Date**

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Visvesvaraya Technological University

Jnana Sangama, Belagavi – 590 018.

Prof. Satish Annigeri Ph.D
Registrar (Evaluation)

Phone : (0831)2498133
Fax : (0831)2405461

Ref.No/VTU/Exam/QPDS/KPTCL/2020/ 122

Date: 16 MAR 2020

STRICTLY CONFIDENTIAL

To,
Dr. Eranna,
Associate Professor, EEED,
Dr.AIT, Bengaluru.

Sir/Madam,

By the direction of Vice-Chancellor, Visvesvaraya Technological University, Belagavi, you have been assigned the Confidential Work of KPTCL. You are requested to report to Registrar (Evaluation) Office, Jnana Sangama, VTU campus, Belagavi on 17th March, 2020.

You may need to stay 2 to 3 days in VTU, Head Quarter at Belagavi. The necessary arrangements for your accommodation are made at Guest House, VTU. The honorarium and TA will be provided as per norms.

You are requested to acknowledge the receipt of this mail as confirmation.

Registrar (Evaluation)
VTU, Belagavi

Copy to:
1. The Principal, Dr.AIT, Bengaluru.

MHRD G.O. No. F.9-1/2002-U.3 dt 21.01.03 UGC No. F.6-6/2002/(CPP-1) dt. 13.3.03

28

Dr. M.G.R.
EDUCATIONAL AND RESEARCH INSTITUTE
UNIVERSITY

(Declared as Deemed to be University u/s 3 of UGC Act, 1956)
(An ISO 9001 : 2008 Certified Institution)
Maduravoyal, Chennai 600 095, Tamilnadu, India

ACCREDITED WITH GRADE A NAAC

PhD thesis Evaluator/Examiner

FORM NO.-/ RES/ 046

Ref: No. DRMGRU/ **EE12D005** **Date: 27.11.2017**

To

1. **Dr. K. Sujatha** **Supervisor/Convener**
   Professor
   Department of Electrical and Electronics Engineering
   Dr. M.G.R Educational and Research Institute University
   Maduravoyal, Chennai-600 095
   Ph.No: 9444101291

2. **Dr. Shankaralingappa C.B** **Indian Examiner**
   Professor
   Department of Electrical and Electronics Engineering
   Dr. Ambedkar Institute of Technology
   Nagarbhavi, Bengaluru - 560056
   Ph.No: 9916591835/8754203355

3. **Dr. K. Senthil Kumar** **University Faculty**
   Professor
   Department of Electronics and Communication Engineering
   Dr. M.G.R Educational and Research Institute University
   Maduravoyal, Chennai-600 095
   Ph.No: 9884277055

Sub: Conduct of the Public Viva-Voce Examination in connection with the thesis for the Ph.D., Degree.

****

As suggested, the Public Viva Voce Examination for **Ms. Sinthia .P** in connection with her thesis submitted for the Ph.D., degree will be held on **04.12.2017** at **11.00 am** in the **Seminar Hall, VOC Block,** in our University.

BOE 2019.20

ಬೆಂಗಳೂರು ವಿಶ್ವವಿದ್ಯಾಲಯ

ಸಂಖ್ಯೆ: ಪ.ವಿ 1/ಪ ಮಂ ME/PES/ಸದಸ್ಯ/2019-20.

ಕುಲಸಚಿವರು (ಮೌಲ್ಯಮಾಪನ)
ಪರೀಕ್ಷಾ ಭವನ,
ಬೆಂಗಳೂರು ವಿಶ್ವವಿದ್ಯಾಲಯ,
ಬೆಂಗಳೂರು-560 056.
ದಿನಾಂಕ: 25.04.2019.

ಗೆ,
ಡಾ॥ ಶಂಕರ್,
ವಿದ್ಯುತ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗ,
ಡಾ॥ ಅಂಬೇಡ್ಕರ್ ತಾಂತ್ರಿಕ ಕಾಲೇಜು,
ಮಲ್ಲತ್ತಹಳ್ಳಿ,ಬೆಂಗಳೂರು-560056.

ಮಾನ್ಯರೇ,

ವಿಷಯ: 2019-20ನೇ ಸಾಲಿನ ಸ್ನಾತಕ ಇಂಜಿನಿಯರಿಂಗ್ ಪರೀಕ್ಷಾ ಮಂಡಳಿಗೆ ಸದಸ್ಯರನ್ನಾಗಿ ನೇಮಕ ಮಾಡುವ ಬಗ್ಗೆ.

ಉಲ್ಲೇಖ: 1. ದಿನಾಂಕ 26-03-2019 ರಂದು ಜರುಗಿದ ಪರೀಕ್ಷಾ ನೇಮಕಾತಿ ಮಂಡಳಿ ಸಭೆಯ ನಡವಳಿಗಳು.
2. ಮಾನ್ಯ ಕುಲಪತಿ ಅನುಮೋದನೆ, ದಿನಾಂಕ:26-03-2019.

*****

ಸಿಂಡಿಕೇಟ್ ಸಭೆಯ ನಿರ್ಣಯವನ್ನು ಕಾಯ್ದಿರಿಸಿ, ಈ ಕೆಳಕಂಡ ಸ್ನಾತಕ ಇಂಜನಿಯರಿಂಗ್ ಪರೀಕ್ಷಾ ಮಂಡಳಿಯ ಸದಸ್ಯರಾಗಿ ತಾವು ನೇಮಕಗೊಂಡಿದ್ದಿರೆಂದು ತಿಳಿಸಲು ನಾನು ನಿರ್ದೇಶಿತನಾಗಿದ್ದೇನೆ.

ವಿಷಯ:- PG - M.E./ M. Tech-Electrical Engineering -Power & Energy Systems ಜೂನ್ 2019 ಮತ್ತು ಜನವರಿ 2020 ಪರೀಕ್ಷೆಗಳು.

ವಿಶ್ವವಿದ್ಯಾಲಯದ ಬೋಧನೆ ಅಥವಾ ಸಂಶೋಧನೆಯು ಕಾಲೇಜು ಶಿಕ್ಷಣದ ಅಥವಾ ಸಮಾನ ಮಟ್ಟದಲ್ಲಿ ತೊಡಗಿಸಿಕೊಂಡಿರುವಿರೆಂದು ಗ್ರಹಿಸಿ ವಿಶ್ವವಿದ್ಯಾಲಯದ ಪರೀಕ್ಷಾ ಆಧ್ಯಾದೇಶ 2011ರಲ್ಲಿ ನಮೂದಿಸಲಾದ ಯಾವುದೇ ಅನರ್ಹತೆ ಗುರಿಗೆ ಒಳಗಾಗಿರುವುದಿಲ್ಲೆಂದು ಭಾವಿಸಿ ಈ ನೇಮಕಾತಿ ಮಾಡಲಾಗಿದೆ.

ತಾವು ಪರೀಕ್ಷಾ ಮಂಡಳಿ ಅಧ್ಯಕ್ಷರು ಕರೆಯುವ ಪರೀಕ್ಷಾ ಮಂಡಳಿ ಸಭೆಗೆ ಹಾಜರಾಗಿ ಪ್ರಶ್ನೆಪತ್ರಿಕೆಗಳನ್ನು ಪರಿಶೀಲಿಸಿ ಎಲ್ಲಾ ಮಂಜೂರು ಮಾಡುವುದು. ಒಂದು ವೇಳೆ ಆಂತರಿಕ ಪರೀಕ್ಷಕರಾಗಿದ್ದರೆ ವಿಶ್ವವಿದ್ಯಾಲಯದ ಪರೀಕ್ಷೆಗಳಿಗೆ ಸಂಬಂಧಿಸಿದ ಮುಖ್ಯ ಅಧೀಕ್ಷರ/ ಉಪಅಧೀಕ್ಷರ/ ಕೊಠಡಿ ಅಧೀಕ್ಷರಾಗಿ ಕಾರ್ಯ ನಿರ್ವಹಿಸುವ ಷರತ್ತಿಗೆ ಬದ್ಧರಾಗಿರತಕ್ಕದ್ದು. ಈ ಕೆಳಕಂಡ ನಿಮ್ಮ ಪರೀಕ್ಷಾ ಮಂಡಳಿಯ ಅಧ್ಯಕ್ಷರೊಂದಿಗೆ ಪರೀಕ್ಷಾ ಕಾರ್ಯಗಳನ್ನು ಯಶ್ವಸಿಯಾಗಿ ಜರುಗಲು ಸಹಕರಿಸ ಬೇಕಾಗಿ ಕೋರಲಾಗಿದೆ.

--ಪುಟ-2-

BOE - 2016-17

BANGALORE UNIVERSITY

UNIVERSITY VISVESVARAYA COLLEGE OF ENGINEERING

B.C. SUJATHA
Associate Professor,
Chairperson, BOE-PG(PES &PE)
June/July 2017 Examination

Mobile : 9886200252

Department of Electrical Engineering
K R Circle, Bangalore-560 001

Date :31-05-2017

To,
Dr. Shankar
Professor, Dept of Electrical Engg.
Dr AIT, Bangalore.

Sir/Madam,

Sub : BOE meeting of PG in Power & Energy Systems and Power Electronics – reg

BOE meeting of PG in Power & Energy Systems and Power Electronics is convened on **12th June 2017 at 11 AM** in the Department of Electrical Engineering, UVCE, Bangalore, to scrutinize the Question Paper manuscripts of June/July 2017 Examination.

I request you to kindly make it convenient to attend the meeting.

Thanking you.

Yours faithfully,

(B.C.SUJATHA)

2016-17

INDIAN STATISTICAL INSTITUTE

BANGALORE CENTRE

Telephone: 91-80-2698 5459/440
E-mail : courses@isibang.ac.in
Fax: 91-80-2848 4265

Prof. Siva Athreya
Associate Dean
8th Mile, Mysore Road, R.V College Post
Bangalore 560 059, India

22nd June 2017

Dr. C.B Shankar
Department of Electrical and Electronics Engineering,
Dr. Ambedkar Institute of Technology,
Nagarbhavi,
Bengaluru

Dear Dr. C.B Shankar,

I am writing this letter as Associate Dean at the Indian Statistical Institute, Bangalore.

We are conducting interviews for candidates in reserved OBC (NCL) category for:-

B. Math (Hons.) course on 23rd June 2017 [9.30 - 13.00]

I would like to request your presence in the role of observer for the reserved OBC (NCL) category. The presence of an observer to ensure that the process is fair, is as required by the rules of institute.

Thanking you.

(Siva Athreya)
Associate Dean
Bangalore.Centre

Wayanamac Education Trust

2016-17

DON BOSCO INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi; Affiliated to VTU, Belgaum)
Kumbalagodu, Mysore Road, Bangalore 560 074. INDIA
Phone : +91 80 2843 7028 / 29 / 30, Fax : +91 80 2843 7031. URL : www.dbit.co.in

APRECIATION

Management of Don Bosco Institute of Technology, Bangalore has appreciated **Dr. Shankar. C. B Professor, Dr. Ambedkar Institute of Technology** who has appeared as Expert member & Conducted Interview for the post of Professor/Assoc Professor/Asst. Professor for Electrical & Electronics Engineering Department on July 8th 2017.

Thanks & Best Regards

8/7/17

Dr. R. Prakash

Principal

PRINCIPAL
Don Bosco Institute of Technc
Kumbalagodu. Mysore Roa
Bengaluru 560 074.

Wayanamac Education Trust

DON BOSCO INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi; Affiliated to VTU, Belgaum)
Kumbalagodu, Mysore Road, Bangalore 560 074. INDIA
Phone : +91 80 2843 7028 / 29 / 30, Fax : +91 80 2843 7031. URL : www.dbit.co.in

Date: 30.06.2017

To,
Dr. Shankar C.B
Professor,
Department of EEE
Dr. A.I.T
Bangalore.

Dear Sir,

I am happy to invite you to be a member of Selection Committee as a subject expert for the selection of Professor/Associate Professor/Assistant Professor in the Department of Electrical & Electronics Engineering of our Institute.

The Interview is scheduled to be held on Saturday July 8th, 2017 at 9.A.M.

I look forward to your kind acceptance and co-operation in the selection process.

Thanking you,

Your's Sincerely

6/7/17

PRINCIPAL

PRINCIPAL
Don Bosco Institute of Technology
Kumbalagodu, Mysore Road
Bengaluru-560 074.

2016-17

Wayanamac Education Trust

DON BOSCO INSTITUTE OF TECHNOLOGY

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Phone : +91 80 2843 7028 / 29 / 30, Fax : +91 80 2843 7031. URL : www.dbit.co.in

05/01/2017

LETTER OF APPRECIATION

Dear Dr. Shankar C Byalihal,

We take great pleasure in writing this letter of appreciation on behalf of the Management, Department of EEE & ICCTEST-2017 for being the session chair in the International Conference on "**Current Trends in Engineering, Science & Technology**" @ DBIT, Bengaluru on January 5th 2017.

The session chaired by you on January 5th 2017 was more informative and highly useful for all the participants.

We would like to place on record our profound gratitude and thanks for having rendered the most endearing chaired session by you with high sense professionalism.

5/1/17

Mr. Anguraja R
Prof. & HOD-EEE

Dr. R. Prakash
Principal

2016-17

RAMAIAH
INSTITUTE OF
TECHNOLOGY

M S Ramaiah Nagar
MSRIT Post
Bangalore 560 054
www.ramaiah-india.org

T 080 2360 3122/0822
F 080 2360 3124
E principal@msrit.edu
W www.msrit.edu

Ref.No.: MSRIT/

Date .....

09th May, 2017

To

Dr. Shankar C B,
Professor, Department of EEE,
Dr.AIT, Bangalore.

Dear Sir,

On behalf of the Department of Electrical & Electronics Engineering of MSRIT, we wish to thank you for being a judge during the open house project exhibition - Pradharshana2017 held on 9th May, 2017.

We appreciate the time you took out of your busy schedule to evaluate our students' projects and for giving your valuable suggestions to them. We look forward to your participation in future events at MSRIT as well.

Thanking you,

Yours faithfully,

HEAD
Electrical & Electronics Dept
Ramaiah Institute of Technology
BANGALORE - 560 054.

Item No: 28

Name of Faculty: NALINI.S

Designation: Assoc. Prof.

Department: EEE

NALINI.S SL. No. 28

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE – 560056

(Aided by Govt. of Karnatak and an Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum, Karnataka, India)

OFFICE OF THE DEAN (EXAMINATIONS)

Dr. Prashanth C R, Dean(Examination)

Ph: +91 9448200979

Ref: Dr.AIT/Dean(E)/ BOE/383/2021-22

Date: 13.1.2022

STRICTLY CONFIDENTIAL

To:

Dr. /Prof. NALINI.S

EEE, DrAIT

Sir/Madam,

Subject: BOE Meeting for the ODD Semester End Examination, SEE – UG/PG, February/March – 2022.

It is a great pleasure to appoint your kind self as BOE Chairman/Member for the odd Semester End Examination. In this regard, I would like to inform you that BOE meeting for the odd Semester End Examination **February/March – 2022** is convened on 17/1/2022 **at 9.30 AM/2.00 PM**, to scrutinize the question papers of EEE branch. The meeting is arranged in Autonomous Examination Section of our Institution. Kindly accept our invitation to be the BOE as Chairman / Member and make it convenient to attend the BOE meeting without fail.

If you are unable to accept our invitation to be the BOE as Chairman / Member, kindly inform the undersigned in advance.

Thanking you,

Yours Sincerely,

DEAN (Examinations)
Dr. Ambedkar Institute of Technology
(An Autonomous Institute)
Bengaluru - 560 056.

Dr. Ambedkar Institute of Technology, Bengaluru
(Estd. 1980(An Autonomous Institution affiliated to VTU, Belagavi,
Accredited by NAAC with 'A' Grade)

OFFICE OF THE DEAN (ACADEMIC)

Dr.AIT/Dean (A)/896 /2021-22 17.12.2021

CIRCULAR

Subject: Induction Programme for 1st year B.E (AY:2021-22) – reg.

With reference to the above subject all the HoD's are hereby informed to note the following:

- The Induction programme for 1st year B.E (AY: 2021-22) will be scheduled from 20th Dec 2021 to 26th Dec 2021.
- On Monday, 20th December 2021, all the 1st year BE students will be visiting the Institute for familiarization with their respective departments and the common facilities of the Institute.
- HoD's shall nominate the faculty co-ordinators to guide the students on 20th December 2021.
- During the visit, COVID-19 Protocols should be followed by the students, faculty and staff.
- The schedule to be followed on Monday, 20th December 2021 is enclosed.

Dr. JPK 18/12/21
&
NS 18/12/21

K.N. 17/12/21
Dean (Academic)

Dean (Academic)
Dr. Ambedkar Institute of Technology
Bengaluru-560 056.

To: All the HODs, Dr.AIT

Copy to: 1)The Principal for the kind information
2) The Hon'ble Secretary, PVPWT for kind information
3) The Mentor, Dr.AIT for kind information
4) Dean (Examination) for kind information

Dr.AIT/Dean(A)/896/2021-22

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY
INDUCTION PROGRAMME FOR 1st YEAR B.E. STUDENTS
Programme Schedule on Monday 20th December 2021

Familiarization with College, Dept. /Branch

Time	Dept./Branch	Activities
9.30 AM to 11.0O AM	CV, ME, AE, CS, IS, EE, ECE, EI, ETE, ML, IEM (Students visit to their respective departments)	• HoDs /coordinators presentation about the department. • Interaction with faculty members • Visit to UG Lab, Research lab etc.

Time	BRANCH						
	CIVIL	ME & AE	ECE	ETE & ML	CSE	EEE & IEM	ISE & EI
11.00 AM to 11.40AM	Data centre, Hostel (BOYS / GIRLS)	Library	Canara Bank / Canteen	Physics, chemistry, HSS department	Mathematics, MBA, MCA	Autonomous Exam section, Auditorium	Placement cell
11.40 AM to 12.20 PM	Library	Canara Bank / Canteen	Physics, chemistry, HSS department	Mathematics, MBA, MCA	Autonomous section, Auditorium	Placement cell	Data centre, Hostel (BOYS / GIRLS)
12.20 PM to 01.00 PM	Canara Bank / Canteen	Physics, chemistry, HSS department	Mathematics, MBA, MCA	Autonomous section, Auditorium	Placement cell	Data centre, Hostel (BOYS / GIRLS)	Library
LUNCH BREAK: 1.00 PM – 1.45 PM							
1.45 PM to 2.25 PM	Physics, chemistry, HSS, department	Mathematics, MBA, MCA	Autonomous section, Auditorium	Placement cell	Data centre, Hostel (BOYS / GIRLS)	Library	Canara Bank / Canteen
2.25 PM to 3.00 PM	Mathematics, MBA, MCA	Autonomous section, Auditorium	Placement cell	Data centre, Hostel (BOYS / GIRLS)	Library	Canara Bank / Canteen	Physics, chemistry, HSS department
3.00 PM to 3.40 PM	Autonomous section, Auditorium	Placement cell	Data centre, Hostel (BOYS / GIRLS)	Library	Canara Bank / Canteen	Physics, chemistry, HSS department	Mathematics, MBA, MCA
3.40 PM to 4.15 PM	Placement cell	Data centre, Hostel (BOYS / GIRLS)	Library	Canara Bank/Canteen	Physics, chemistry, HSS department	Mathematics, MBA, MCA	Autonomous section, Auditorium

Note: COVID 19 Protocols should be followed by the students and faculty members

Dean (Academic)
Dr. Ambedkar Institute of Technology

RASHTREEYA SIKSHANA SAMITHI TRUST
RV COLLEGE OF ENGINEERING
(Autonomous Institutions Affiliated to VTU, Belagavi)
8th Mile, Mysore Road, Bengaluru-560059

Dr G Sadashivappa
Controller of Examinations

Phone : 080-6818-8116
Mobile : 9901528064
EMail : coe@rvce.edu.in

STRICTLY CONFIDENTIAL

Date: 03.12.2021

To,

Prof. NALINI
ASSISTANT PROFESSOR
Dr.AIT, Bengaluru
Sir/Madam

Sub: Appointment as paper setter for UG Examinations

It is my pleasure to inform you that you are appointed as paper setter for UG examinations of . I request you to set the paper for the course as per the details given below.

Semester	Semester-7
Branch/Program	ELECTRICAL AND ELECTRONICS ENGINEERING
Course code	16EE72
Course title	SWITCH GEAR AND PROTECTION
Last date for submission : 11.12.2021	

Guidelines for paper setting:

1 Set Question paper as per the scheme given at the end of syllabus.
2 Scheme of question Paper - the question paper should contain two parts Part-A and Part-B. Part- A is for 20 marks convering the complete syllabus and is compulsory. Question should be numbered as 1.1 1.2 1.3... with a maximum of 2 marks for each question. multiple choice questions and true or false questions are not permitted.
3 Questions in Part -B should consist of 7 questions with qustion number 2 to 8 . the question number 2 from unit 1 (without choice) question number 3 & 4 from unit 2(with internal choice) question number 5 & 6 from unit 3(with internal choice) and question number 7 & 8 from unit 4 & 5(without choice). please refer the model question paper.
4 Question paper should cover entire syllabus and as per the requirement of Outcome Based Education (OBE) with the questions against different cognitive levels of Blooms Taxanomy. A Sample list of Action verbs to be used under different levels is shown Overleaf.

Bloom's Level	Weightage as a percent of total marks
Questions with Bloom's cognitive levels 1 and 2	40%
Questions with Bloom's cognitive levels 3 and 4	40%
Questions with Bloom's cognitive levels 5 and 6	20%

5 There shall be a maximum of FOUR sub division under each question. Numerical problems shall be set wherever possible.
6 Provide neat sketches if any drawn with black ink and with relevent quetion number
7 Provide Instruction on issue of handbooks steam tables charts etc. when absolutely necessary.
8 Furnish scheme and solution in the sheets provided. Question paper without scheme and solution is liable for rejection
9 The scheme and solution should contain detailed step by step solution along with distribution of marks accounting for the total marks alloted for the question. Questions seeking descriptive answers the important points to be covered are to be mentioned; mere breakup of marks will not be sufficient.
10 Set question such that they demand specific answers.
11 Indicate any alternate answer or solution to a problem (if any) in the scheme.
12 Ensure that no hard or soft copy of any material relevent to question paper is retained by paper setter
13 Kindly return the declaration form duly signed
14 Prepare and enclose the bill as per the remuneration shown in the following table.

Remuneration	
Program	**Remuneration (Rs.) Paper Setting , Scheme & Solution**
B.E/B.Arch	1700/-(1000/- for paper setting + 700/- for scheme and solution)

Postal charges of Rs. 30/- may be claimed if the manuscript is sent by post.
A copy of syllabus containing the scheme of question paper and list of reference books is enclosd herewith.
Enclose the manuscript of question paper and scheme & solution in seperate covers provided.

Thanking You

With regards

(Dr G Sadashivappa)
Controller of Examinations

Encls :
1. Copy of the Syllabus
2 Manuscript sheets
3 Sheets for scheme and solution
4. Inner cover for Manuscript
5. Inner cover for scheme and solution
6. Outer cover for question paper,scheme and solution
7. Declaration form
8. Model question or previous exam question paper

Definitions and action verbs associated with Blooms cognitive levels

Blooms Level	Remember Level 1	Understand Level 2	Apply Level 3	Analyze Level 4	Evaluate Level 5	Create Level 6
Definitions of cognitive levels	Exhibit memory of previously learned material by recalling facts, terms,basic concepts and answers.	Demonstrate understanding of facts and idea by organizing , comparing, translating, interpreting, giving descriptions and stating main ideas	Solve problems to new situations by applying acquired knowledge, facts, technique and rules in different way.	Examine and break information into parts by identifying motives or causes, make interferences and find evidence to support generalizations.	Present and defend opinions by making judgements about information, validity of ideas, or quality of work based on a set of criteria.	Compile information together in a different way to combining elements in a new pattern or proposing alternative solutions.
Action verbs	Describe identify	Explain	Compute	Characterize	Justify	Formulate
	Name Recognize	Compare	Use	Examine	Estimate	Invent
	Find	Discuss	Draw	Derive	Prioritize	Compose
	List	Predict	Illustrate	Identify	Recommend/ Interpret	Design/ Develop
	Where	Distinguish	Classify/Categorize	Organize	Evaluate	Construct
	Write/Draw	Summarize	Solve	Investigate	Decide choose	Predict

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE – 560056

(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum & Aided by Govt. of Karnataka, India)

OFFICE OF THE DEAN (EXAMINATIONS)

Dr. Prashanth C R, Dean (Examinations) Ph: +91 9448200979

Ref: Dr.AIT/Dean(E)/ 307/2021-22 Date: 27-11-2021

CONFIDENTIAL

Dr. /Prof. NALINI.S

Subject: Appointment as Paper Setter for V Semester U.G / P.G Degree (Autonomous) Semester End Examination _____

On behalf of the Principal, Dr. Ambedkar Institute of Technology, Bangalore-560056, it is my pleasure to appoint you as the Paper Setter for setting the question paper for the subject/s shown below:

Sl. No.	Subject Code	Subject Title	No. of Set/s	To be submitted before
1.	18EE 561	Renewable Energy Sources	01	

You are requested to confirm your acceptance over the phone. Your immediate response is highly solicited. **In case, you are unable to accept this assignment, please return all the enclosures to the undersigned by registered/speed post immediately.**

INSTRUCTIONS TO PAPER SETTERS

1. Questions shall strictly be set from Autonomous syllabus of Dr. Ambedkar Institute of Technology, Bangalore-560056.
2. Prescribed text books/reference books mentioned in the syllabus shall only be referred.
3. **Questions shall not be set by mixing the topics from different units.**
4. Two questions have to be set from each unit, so that question paper Contains ten questions for 20 marks each.
5. Provide necessary instructions to students, such as use of data hand books, steam tables, charts, etc.,
6. It is imperative that the question paper shall be prepared by the paper setter only. **Do not preserve or make a copy of the question paper/scheme and solution in any form.**
7. Compulsorily scrutinize the question paper for its completeness, such as subject code, subject title, time duration, maximum marks, instructions to students, marks allocation, etc.,
8. **Ensure complete confidentiality.**
9. **For scheme of examination (Question paper pattern), a copy of the syllabus and model question paper is enclosed herewith for your reference.** You can contact the Dean (Examinations)for any other details, if required.
10. Prepare (i) Handwritten manuscripts of the question paper,(ii) Softcopy of the question paper shall be submitted to the Office of the Dean (Examinations). **Please ensure that question paper is attested by your signature with date**
11. **In case, sketches are not drawn using MS-WORD, then provide neat sketches using black pen in separate sheets.** Leave sufficient space between sketches. **Do not draw sketches along with the running matter. Sketches are directly scanned from the manuscript for final printing.**
12. The question paper in MS-Word format has to be sent to the Email ID: qp.aes@drait.edu.in The Subject Title & Code shall be clearly mentioned while composing the email. Question paper file name shall be subject code.
13. Question paper should cover complete syllabus and as per the Revised Bloom's Taxonomy. A sample list of action Verbs to be used under different levels are shown at the end of this letter.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE – 560056

(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum & Aided by Govt. of Karnataka, India)

OFFICE OF THE DEAN (EXAMINATIONS)

Dr. G. Rajendra, Dean (Examinations) Ph: +91 9845659168

Ref: Dr.AIT/Dean(E)/523/2020-21 Date: 23/06/2021

CONFIDENTIAL

Dr. /Prof. Nalini S

Subject: Appointment as Paper Setter for ___ Semester U.G / P.G Degree (Autonomous) Semester End Examination _____

On behalf of the Principal, Dr. Ambedkar Institute of Technology, Bangalore-560056, it is my pleasure to appoint you as the Paper Setter for setting the question paper for the subject/s shown below:

Sl. No.	Subject Code	Subject Title	No. of Set/s	To be submitted before
1.	18EE23	BEE	01	10/7/21

You are requested to confirm your acceptance over the phone. Your immediate response is highly solicited. **In case, you are unable to accept this assignment, please return all the enclosures to the undersigned by registered/speed post immediately.**

INSTRUCTIONS TO PAPER SETTERS

1. Questions shall strictly be set from Autonomous syllabus of Dr. Ambedkar Institute of Technology, Bangalore-560056.
2. Prescribed text books/reference books mentioned in the syllabus shall only be referred.
3. **Questions shall not be set by mixing the topics from different units.**
4. Two questions have to be set from each unit, so that question paper Contains ten questions for 20 marks each.
5. Provide necessary instructions to students, such as use of data hand books, steam tables, charts, etc.,
6. It is imperative that the question paper shall be prepared by the paper setter only. **Do not preserve or make a copy of the question paper/scheme and solution in any form.**
7. Compulsorily scrutinize the question paper for its completeness, such as subject code, subject title, time duration, maximum marks, instructions to students, marks allocation, etc.,
8. **Ensure complete confidentiality.**
9. **For scheme of examination (Question paper pattern), a copy of the syllabus and model question paper is enclosed herewith for your reference.** You can contact the Dean (Examinations)for any other details, if required.
10. Prepare (i) Handwritten manuscripts of the question paper,(ii) Softcopy of the question paper shall be submitted to the Office of the Dean (Examinations). **Please ensure that question paper is attested by your signature with date**
11. **In case, sketches are not drawn using MS-WORD, then provide neat sketches using black pen in separate sheets.** Leave sufficient space between sketches. Do not draw sketches along with the running matter. **Sketches are directly scanned from the manuscript for final printing.**
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RV COLLEGE OF ENGINEERING®

(Autonomous Institution Affiliated to VTU,Belagavi)
8th Mile, Mysore Road,Bengaluru-560059

Dr G Sadashivappa
Controller of Examinations

Phone:080-67178163/8166
Mobile: 9845934591
Email:sadashivappag@rvce.edu.in

// RVE/EXAM/ 160/2020-21

Date:30.12.2020

STRICTLY CONFIDENTIAL

To,

Prof. S Nalini
Professor
Dr. Ambedkar Institute of Technology

Sir/Madam

Sub: Appointment as Paper Setter for Jan/Feb-2021 UG Examinations

It is my pleasure to inform you that you are appointed as paper setter for UG Examinations of Jan/Feb-2021. I request you to set the paper for the course as per the details given below:

Semester	7
Branch/Program	Electrical and Electronics Engineering
Course code	16G7H06
Course title	DESIGN OF RENEWABLE ENERGY SYSTEMS
Last date for submission :15-01-2021	

Guidelines for paper setting:

1 Set Questions as per the scheme given at the end of syllabus.

2 Scheme of question paper-The question paper should contain two parts Part-A and Part-B. Part-A is for 20 marks covering the complete syllabus and is compulsory. Questions should be numbered as 1.1 1.2 1.3... with a maximum of 2 marks for each question. Multiple choice questions and true or false questions are not permitted.

3 Questions in Part-B should consist of 7 questions with question number 2 to 8. The question number 2 from unit 1(without choice) question number 3 & 4 from unit 2(with internal choice) question number 5 & 6 from unit 3(with internal choice) and question numbers 7 & 8 from unit 4 & 5 respectively(without choice). Please refer the model question paper.

4 Question paper should cover entire syllabus and as per the requirement of Outcome Based Education(OBE) with the following distribution of questions against different cognitive levels of Blooms Taxanomy. A sample list of Action verbs to be used under different levels is shown overleaf.

Bloom's Level	Weightage as a percent of total marks
Questions with Bloom's cognitive levels 1 and 2	40%
Questions with Bloom's cognitive levels 3 and 4	40%
Questions with Bloom's cognitive levels 5 and 6	20%

5 There shall be a maximum of FOUR sub divisions under each question. Numerical problems shall be set wherever possible.

6 Provide neat sketches if any drawn with black ink and with revelent question number as these figures are scanned directly from the manuscript.

7 Provide instrcution on issue of handbooks steam tables charts etc. when absolutely necessary.

8 Furnish scheme and solution in the sheets provided. Question paper without scheme and solution is liable for rejection.

9 The scheme and solution should contain detailed step by step solution along with distribution of marks accounting for the total marks alloted for the question. For questions seeking descriptive answers the important points to be covered are to be mentioned; mere breakup of marks will not be sufficient.

10 Set questions such that they demand specific answers.

[illegible]cate any alternate answer or solution to a problem (if any) in the scheme.

[illegible]at no hard or soft copy of any material relevant to question paper is retained by you.

[illegible]eturn the declaration form duly signed

[illegible]re and enclose the bill as per the remuneration shown in the following table.

Siddaganga Institute of Technology

(An autonomous Institute affiliated to VTU, Belagavi. Approved by AICTE, New Delhi)

B.H. Road, Tumakuru – 572 103, Karnataka.

Dr. M. Siddalinga Prasad
Controller of Examinations

Phone : 0816-2214052
Fax : 0816-2282994
e-mail : coe@sit.ac.in

Ref. No. COE/ES/EPE08/2019-2020 Date: 10 June 2020

To
Ms. Nalini S
Associate Professor
Dept. of Electrical Engg.
Dr. Ambedkar Institute Of Technology
Near Jnana bharathi Campus
Bangalore 560 056

Dear Madam,

Sub: Question paper setting for the M.Tech. Examinations of **August 2020** – reg.

I request you kindly to set one question paper of **Second** semester **M.Tech. Power Electronics** in the subject **Power Electronics Application to Renewable Energy System [2REPEE06]** for the examination to be held in **August 2020** and send the ***legibly hand written manuscript*** of the question paper **(Scheme and Solution NOT REQUIRED)** in a sealed cover on or before **24 June 2020.**

Kindly Acknowledge the Receipt of this Request by email, at the earliest.

Thank you,
Warm regards,

Controller of Examinations

Instructions:

1. Question Paper shall consist of five parts [Unit-I to Unit-V] corresponding to each unit of the syllabus.
2. Each part shall have two questions and student has to answer one full question from each part.
3. Each question shall have preferably two to four sub divisions, covering the entire syllabus of that unit. [Refer to model question paper enclosed].
4. **It is Preferable to set the question paper according to Bloom's Taxonomy (PTO for Bloom's Taxonomy Action Verbs)**
5. **Sketches, figures and tables if any should be clearly drawn, as the same is scanned for printing.**
6. The question paper should contain all the data / figures / marks allocated, with clarity.
7. Model Question paper / Question paper of previous examination is enclosed for your reference.
8. Please sign the remuneration bill enclosed and return by placing it in the outer cover. ***Please Provide Bank Name, Bank Account Number, IFSC Code and PAN in the space provided in the remuneration bill***

Enclosures: Copy of syllabus

Model Question Paper / Previous question paper.
Manuscript sheets and cover.
Remuneration bill.
Outer cover.

Please Turn Over

RASHTREEYA SIKSHANA SAMITHI TRUST
RV COLLEGE OF ENGINEERING
(Autonomous Institutions Affiliated to VTU, Belagavi)
8th Mile, Mysore Road, Bengaluru-560059

Dr G Sadashivappa
Controller of Examinations

Phone : 080-6818-8116
Mobile : 9901528064
EMail : coe@rvce.edu.in

STRICTLY CONFIDENTIAL

Date: 24.11.2021

To,

Prof. HARINI VAIKUND
ASSISTANT PROFESSOR
Dr.AIT, Mallathalli
Sir/Madam

Sub: Appointment as paper setter for UG Examinations

It is my pleasure to inform you that you are appointed as paper setter for UG examinations of . I request you to set the paper for the course as per the details given below.

Semester	Semester-7
Branch/Program	ELECTRICAL AND ELECTRONICS ENGINEERING
Course code	18EE7F1
Course title	COMPUTER COMMUNICATION AND NETWORKING
Last date for submission : 02.12.2021	

Guidelines for paper setting:

1 Set Question paper as per the scheme given at the end of syllabus.
2 Scheme of question Paper - the question paper should contain two parts Part-A and Part-B. Part- A is for 20 marks convering the complete syllabus and is compulsory. Question should be numbered as 1.1 1.2 1.3... with a maximum of 2 marks for each question. multiple choice questions and true or false questions are not permitted.
3 Questions in Part -B should consist of 7 questions with qustion number 2 to 8 . the question number 2 from unit 1 (without choice) question number 3 & 4 from unit 2(with internal choice) question number 5 & 6 from unit 3(with internal choice) and question number 7 & 8 from unit 4 & 5(without choice). please refer the model question paper.
4 Question paper should cover entire syllabus and as per the requirement of Outcome Based Education (OBE) with the questions against different cognitive levels of Blooms Taxanomy. A Sample list of Action verbs to be used under different levels is shown Overleaf.

Bloom's Level	Weightage as a percent of total marks
Questions with Bloom's cognitive levels 1 and 2	40%
Questions with Bloom's cognitive levels 3 and 4	40%
Questions with Bloom's cognitive levels 5 and 6	20%

5 There shall be a maximum of FOUR sub division under each question. Numerical problems shall be set wherever possible.
6 Provide neat sketches if any drawn with black ink and with relevent quetion number
7 Provide Instruction on issue of handbooks steam tables charts etc. when absolutely necessary.
8 Furnish scheme and solution in the sheets provided. Question paper without scheme and solution is liable for rejection
9 The scheme and solution should contain detailed step by step solution along with distribution of marks accounting for the total marks alloted for the question. Questions seeking descriptive answers the important points to be covered are to be mentioned; mere breakup of marks will not be sufficient.
10 Set question such that they demand specific answers.
11 Indicate any alternate answer or solution to a problem (if any) in the scheme.
12 Ensure that no hard or soft copy of any material relevent to question paper is retained by paper setter
13 Kindly return the declaration form duly signed
14 Prepare and enclose the bill as per the remuneration shown in the following table.

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ. ಕರ್ನಾಟಕ. ಭಾರತ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY,
BELAGAVI. KARNATAKA. INDIA

211110EE25436751

Ref.No/VTU/Exam/2021-2022/918

Date : 17-11-2021

STRICTLY CONFIDENTIAL

MRS.HARINI VAIKUND,Department of EE, DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE

Sir/Madam,

Sub : Appointment as paper setter for **DEC-JAN 2022 EXAMINATIONS.**

By the direction of the Vice-Chancellor, I am pleased to appoint you as paper setter for the subject with details as under. **You are requested to accept the assignment.**

Exam: B.E/B.Tech/B.Arch/M.Arch/MBA/MCA/M.Tech
Course / Branch: EE
Sem: 7
Subject Name: Operating System
Subject Code: 10EE754
No. of sets: 1
To be submitted before: 08-12-2021

INSTRUCTIONS TO PAPER SETTERS (2010 SCHEME)

- **This appointment is assigned a unique ID represented by the barcode accompanied by the ID in human readable form. The first page of the manuscript cannot be used for a different subject or by a different faculty member. In case you have to use a different first page, write the appointment ID by hand prominently.**
- **Typed manuscript / scheme will not be accepted. Please write down the questions LEGIBLY.**
- Use only 'generally accepted' short forms. (It is difficult to decipher 'S.T.' as 'Show that'.)
- Set eight questions, out of which, the students have to answer five full questions. (Unless otherwise specified).
- Wherever syllabus is made unitwise consisting of **8 units** ONE FULL question should be set from each unit, (not applicable to MBA).
- Questions shall **strictly be from the university prescribed books, covering the entire syllabus.** There shall be a **maximum of four subdivisions in each question.** Please avoid subdivisions, within subdivisions, for the sake of clarity. Wherever applicable, there shall be a numerical problem in each question.
- Provide neat sketches as these figures are scanned directly from the manuscript for final printing. Sketches should be drawn in black ink only.
- Explicitly indicate the figure numbers. For example, identify the figure pertaining to Q 2(c) as Fig. Q2 (c) immediately adjacent to it.
- **Whenever problems are included, make sure to give all the relevant data from the data handbook, to avoid the issue of handbooks / tables / charts etc..**
- However, if special charts, data book are required to be given, please explicitly mention the same, in the ' Instruction to the candidates', at top of the Q.P.
- Scrupulously scrutinize the question paper for its completeness (subjectcode, subject title & clarity), missing data / figure / marks allocated / repetition of entire question or part thereof & out of syllabus questions. Read the question paper meticulously and seal it in the appropriate barcode cover enclosed, before sending it to the university.
- Furnish the scheme & solutions, in the enclosed sheet meant for it & seal it in the appropriate cover. Indicate boldly the final answers for all numerical problems.
- A question paper **without "Scheme & Solution" is liable for rejection.**
- **Ensure that the manuscript of the question paper, along with scheme & solution, reaches the undersigned, by Registered post on or before the expiry of the date mentioned above.**
- **Ensure that you personally destroy the rough draft & other material used to prepare the manuscript. No part of the manuscript or the full version should be retained in any form.**
- In case, you are unable to accept the assignment, please return all enclosures to the undersigned by return of post, with valid reasons for not accepting it.

Thanking You,

Yours sincerely,

Registrar (Evaluation)

Encls:

1.Copy of the syllabus.
2.Manuscript sheets
3.Inner cover for the manuscript.
4.Outer barcode cover for QP manuscript & scheme.
5.Inner cover for the scheme & solution.
6.Remuneration bill.
7.Scheme & solution sheet.

RV Educational Institutions
RV College of Engineering
Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum
Approved by AICTE New Delhi

DEPARTMENT OF ELECTRICAL
&
ELECTRONICS ENGINEERING

Phone: 080 - 67178049
080 – 67178050
Fax : 080 –28600337
Email: hod.eee@rvce.edu.in
Website: www.rvce.edu.in
Office of the Principal
R.V.Vidyaneketan Post,
Mysuru Road,
Bengaluru-560 059

DATE: 25.02.2021

CERTIFICATE

This is certify that Smt. Harini Vaikund

Designation: Assistant Professor

Department: Electrical & Electronics Engineering

College : Dr.AIT

Place : Bengaluru

has conducted V sem Minor Project Lab (18EE55) Examination as an External Examiner at this center from 25.02.2021 (01 day).

25/2/21

Dr. Rudranna Nandihalli
Prof. & Head, Dept. of EEE

RV Educational Institutions
RV College of Engineering
Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi
Approved by AICTE New Delhi

DEPARTMENT OF ELECTRICAL
&
ELECTRONICS ENGINEERING

Phone: 080 - 67178049
080 – 67178050
Fax : 080 –28600337
Email: hod.eee@rvce.edu.in
Website: www.rvce.edu.in
Office of the Principal
R.V.Vidyaneketan Post,
Mysuru Road,
Bengaluru-560 059

DATE: 17.03.2021

CERTIFICATE

This is certify that Smt. Harini Vaikund

Designation: Assistant Professor

Department: Electrical & Electronics Engineering

College : Dr.AIT

Place : Bengaluru

has conducted III sem Analog Electronic Circuits Lab (18EE33) Examination as an External Examiner at this center from 17.03.2021 (01 day).

17/3/21

Dr. Rudranna Nandihalli
Prof. & Head, Dept. of EEE

Head
Dept. of Electrical & Electronics Engineering
R.V. College of Engineering
Bangalore - 560 059

VARDHAMAN COLLEGE OF ENGINEERING

(AUTONOMOUS)

(Affiliated to JNTUH, Approved by AICTE and Accredited by NBA, NAAC 'A' Grade)

Shamshabad, Hyderabad – 501 218

EXAMINATION – BRANCH

Date: 17.02.2020

From

CONTROLLER OF EXAMINATIONS

To Harini Vaikund.

Dear Sir / Madam,

I am directed to invite you to act as paper setter in Electrical Measurements & Instrumentation Code No AA213 for the B.Tech ..... Semester Examination and I request you to kindly set the question papers.

Your appointment as paper setter should be kept **STRICTLY CONFIDENTIAL.**

You are requested to set **TWO** sets of question papers on the above mentioned course (preferably typed and figure if any, have to be neatly drawn with the help of black pen) and deliver them either in person or send them in a sealed envelope provided herewith by registered post / speed post / Professional Courier.

You are requested to note the following points while setting the question papers.

B.Tech question paper setting instructions:

- The set of question paper should consist of five Units I, II, III, IV & V. Each Unit will have two questions from their respective Unit of the syllabus and will carry 15 marks each. Each question must have **TWO** sub questions i.e., 1(a), 1(b) with breakup marks.
- Candidates are required to attempt one question from each unit in 3 hours.
- Mention clearly on the top of the question paper manuscript if student requires any data, data books etc, during examination.

General Instructions to the question paper setter:

- All the questions are unambiguous
- All the questions are asked for relevant marks
- Frame questions as per Blooms level of COs. Ensure that the questions cover the entire COs uniformly as far as possible.

- The paper setter shall not disclose his / her appointment and shall not make any correspondence on post cards regarding any matter connected with the examination.

- Paper setter shall submit his/her willingness / unwillingness to the authority within fifteen days from the date of the letter of appointment as paper setter.

- The pattern of question papers should conform to that of the model question paper enclosed. The distribution of marks should be the same as given in the model question paper. Keep the stipulated time, for the examination, in mind while setting the question papers.

P.E.S. COLLEGE OF ENGINEERING

MANDYA – 571 401, KARNATAKA

(An Autonomous Institution Affiliated to VTU, Belgaum)

Dr. K. Narasimha Chary, M.Tech., Ph.D
Controller of Examinations

☎ : 08232 – 220043 Ext : 295, 227577 (Direct)
: 08232 - 222075, : 9844318532

Ref: PESCE/EXAM/COE/2019-20/

Date: 20/-2/ -2020

STRICTLY CONFIDENTIAL

Dr./Prof./Sri./Smt./ .....Harini.....

Sir/Madam,

Sub: Appointment as paper setter for June/July 2020 Examinations.

By the direction of Principal, I am pleased to appoint you as paper setter for the Course with details as under. You are requested to accept the assignment.

Exam: B.E./M.Tech/MCA/MBA.....B.E.....Branch: .....E.&.E.....

Sem .....VI.....Course Title ...Embedded.....Systems.....

Course Code: .....P.17.E.E.6.5.3... No. of sets: ...01...To be submitted before..2/-3/...-2020

INSTRUCTIONS TO PAPER SETTERS

- Set Two full Questions (with internal choice) from each unit and both the questions should cover the entire syllabus in full of that particular unit. Students have to answer all questions. (Refer Model / SEE Question paper)
- Questions shall strictly be from the prescribed books, covering the entire syllabus.
  - There shall be a maximum of four subdivisions in each question. Wherever applicable, there shall be a numerical problem in each question.
  - Provide neat sketches as these figures are scanned directly form the manuscript for final printing. Sketches should be drawn in black ink only.
  - Explicitly indicate the figure numbers. For example, identify the figure pertaining to Q2(c) as Fig Q2(c) immediately adjacent to it.
  - Wherever problems are given, make sure you give all the relevant data from the data handbook to avoid the issue of Hand book / Tables / Charts etc..
- Scrupulously scrutinize the question paper for completeness (course, code, title and clarity), Missing data / figure / marks allocated / repetitions of entire questions or parts thereof & out of syllabus questions and read the question paper meticulously - seal it in appropriate cover enclosed, before sending.
  - Furnish the scheme & solutions in the enclosed booklet meant for it & seal it in the appropriate cover. Indicate boldly the final answers for all numerical problems.
  - A question paper without "Scheme & Solutions" is liable for rejection.
  - Ensure that the Manuscript of the question paper along with Scheme and Solutions reaches the undersigned by Registered Post on or before the expiry of the dead line.
- Ensure that you personally destroy the rough draft & other materials you had prepared to prepare the manuscript. No part of the manuscript or the full version should be retained in any form.
- In case, by any chance, you are unable to accept the assignment, please return all enclosures to the undersigned by return of post with valid reasons for not accepting the assignment.

Thanking you

Yours sincerely

KNChary

Dr. K.Narasimha Chary, M.Tech.,Ph.D
Controller of Examinations
P.E.S.College of Engineering
(An Autonomous Institution under VTU, Belgaum
MANDYA-571 401, Karnataka

Encls:
1. Copy of the syllabus
2. Model/SEE Question paper
3. Manuscript sheets
4. Inner cover for Manuscript
5. Inner cover for Scheme & Solutions
6. Scheme & Solutions format
7. Outer cover for QP Manuscript & Scheme
8. Remuneration bill

DAYANANDA SAGAR COLLEGE OF ENGINEERING

Shavige Malleshwara Hills, Kumaraswamy Layout. Bengaluru-560078

An Autonomous Institute Affiliated To Visvesvaraya Technological University, Belagavi. Approved by AICTE & ISO 9001-2008 Certified

Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

Dr Kiran S
Controller of Examinations

email : coe-dsce@dayanandasagar.edu

DSCE/EXAM/ UG-EE / 2019-2020

Date: 20/09/2019

Strictly Confidential

To
Prof. Harini Vaikund
Designation: Professor
Department of: EEE
College: Dr. AIT

Respected Sir/ Madam,

Sub: Appointment as Paper Setter for Dec 2019-Jan 2020 UG Examinations

It is my pleasure to inform that you are appointed as paper setter for Undergraduate Engineering Examinations of **December 2019 - January 2020.** I request you to kindly set the paper for the Course mentioned below and submit the same by **10th October 2019.**

Level	**UG**
Program	Electrical & Electronics Engineering
Semester	1 3 5 ✓ 7
Course title	Modern Control Theory
Course code	17EE5DCMCT

Guidelines for Paper Setting:

1. Set the Questions as per the Syllabus/Scheme provided and confine it to the syllabus.
2. Question paper should cover entire syllabus as per the requirement of Outcome Based Education with 40% of total marks to bloom's cognitive levels 1 & 2, 40% for bloom's cognitive levels 3 & 4 and 20% to bloom's cognitive levels 5 & 6.
3. **Question ONE:** Question One has **20** sub questions of one marks each covering all modules. Four questions from each module have to be set. The questions have to be Multiple Choice Questions(MCQ's) only each question having 4 choices.
4. There shall be a maximum of four sub questions in each question(Question 2 to 8). Kindly indicate the distribution of marks for each subdivision.
5. Kindly prepare the Question paper in MS-Word Format, burn it in the CD provided, take a print and affix the signature for authentication.
6. There is no internal choice in Modules 1, 2 and 3. Whereas internal choice has to be given in module 4 (Question 5 **OR** 6) and module 5 (Question 7 **OR** 8).
7. If the questions include any sketches kindly draw it in MS-Word or any other software, specify figure no. and include it in the question.
8. Indicate the permissible handbooks, charts, data books wherever necessary.

RASHTREEYA SIKSHANA SAMITHI TRUST

RV COLLEGE OF ENGINEERING ®

(AUTONOMOUS INSTITUTION AFFILIATED TO VTU)

UG

RVE/ EXAM/ FT /19-20

Date: 10/9/19

CONFIDENTIAL

To

Prof. Harini
Asst. Prof., Dept. of EEE
Dr. AIT, Bangalore

Dear Sir/Madam

Sub: Appointment as Examiner for Fast Track Practical Examinations July 2019

I am happy to inform you that you have been appointed as examiner for practical examination and it is my pleasure to invite you to conduct practical examinations as per the details given below. I request you to accept our invitation and help us in the smooth conduct of the examination.

Examination: B E **Semester:** III

Subject with Code: Digital Logic Design with Verilog

Time: 8:30am to 11:30am

Sl. No.	Date	No. of Batches	No. of Candidates	Name of the Co-examiner
1	12/07/2019	01	03	Prof. Raja Vidya Asst. Prof., EEE Dept RVCE

Guidelines:

1. The practical examinations are to be conducted as per the scheme of examination and jointly with the co-examiner
2. Evaluation in each practical examination shall be done as per the guidelines given by the college
3. The award of marks is the joint responsibility of both the examiners and the break-up of marks does not indicate the division of marks between the examiners

Kindly inform well in advance in case you are not able to take up the assignment.
For any further information you may please contact
Phone numbers: SUDARSHAN. B.S
Mobile: 9902924598

Yours sincerely

K. Vasudeva B. 10/7/19

Controller of Examinations

Controller of Examinations
R.V. College of Engineering (Autonomous)
Mysore Road, Bangalore - 560 059

RASHTREEYA SIKSHANA SAMITHI TRUST
R.V.COLLEGE OF ENGINEERING
(Autonomous Institution Affiliated to VTU, Belgaum)
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGG.
Office of the Prof. & Head, R.V. Vidyaneketan Post, BANGALORE-560059
Approved by AICTE, New Delhi
Affiliated to VTU, Belgaum, Karnataka
Accred ted by NBA, New Delhi
Phone: 67178020
67178021
67178099
Fax : 28600337
Ext : 8049, 8050
Email: ee@rvce.edu.in
Website: www.rvce.edu.in
RSST website :
www.rviasitutions.com

Ref : RVE/EE/ /2019-20 Date : 12.07.2019

CERTIFICATE

This is certify that Sri/Smt: Harini Vaikund

Designation : Assistant Professor

Department : Electrical & Electronics Engineering

College : Dr. AIT

Place : Bengaluru

has attended as a external examiner to conduct Fast Track Practical Lab July 2019 Examination of MCT lab (16EE64) for VI semester students, DSP lab (12EE52) for V semester students & DLD Lab (16EE34) for III semester students on 12.07.2019(01 day).

12/07

Dr. Rudranna Nandihalli

Prof. & Head, Dept. of EEE

Prof. & Head
Department Electrical
Electronics Engineer
R.V. College Engi
Bangalore - 560 059

30/19-20

DAYANANDA SAGAR COLLEGE OF ENGINEERING

BANGALORE-560078, KARNATAKA, INDIA

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(IC RTEETIMP-2020) **7 - 9 December 2020**

Jointly Organized by Circuit Branches of ETCE, ECE, EEE, EIE, MLE & Physics

Certificate of Participation & Paper Presentation

This is to certify that the author ***JYOTHI P KOUJALAGI*** of ***Dr. AIT, Bengaluru,*** has participated & presented the paper on the topic titled, ***"Comprehensive Review of Winding Configuration in Switched Reluctance Motor"*** and was given the **best paper award** in the session ***7 (2 PM – 4 PM)*** on ***9-12-2020*** in the ***3-Day International Virtual Conf. on Recent Trends in Electrical, Electronics, Telecommunications, Instrumentation Engg., Medical Electronics Engg. & Physics"*** conducted from 7 - 9th of Dec. 2020 & organized by Departments of ECE, ETCE, EIE, EEE, MLE, Physics & the professional society chapters of IEEE, IETE, ISOI, IQAC & MHRD's IIC.

Dr. A.R. Aswatha
Organizing Secretary, IC RTEETIMP-2020
Dean (Academics), Prof. & Head, ETCE, DSCE

Dr. CPS Prakash
Principal
Dayananda Sagar College of Engg.

Comprehensive Review of Winding Configuration in Switched Reluctance Motor

[1]Shivalingaiah. K L, [2]Dwarakanath.M,[3] Jyothi P Koujalagi

[1] Associate Professor, Department of EEE, RajaRajeshwari College of Engineering, Bengaluru, India.
[2] Assistant professor, Department of EEE,Sri Jagadguru Balagangadaranatha Swamy Institute of Technology.
[3] Professors, Department of EEE, Dr. Ambedkar Institute of Technology, Bengaluru.
[1] shivamanikls@gmail.com [2] skdwaraka@gmail.com [3] jyotipkoujalagi@gmail.com

Abstract- *This paper presents design of a concentrated winding configuration of a 6/4 Switched Reluctance Motor to improve torque and efficiency. The objective of this paper is to analyze the torque and efficiency characteristics with different configuration of windings and their influence on total losses of SRM comparing it with Switched reluctance Motor no changes in winding structure. The static characteristics and the flux linkages for different winding configurations are analyzed by finite element method using ANYSYS software. Performance is analyzed at various speeds, efficiency and output power is compared. The total loss in Switched Reluctance Motor with Double layer winding is very less compared with the Single layer winding- Switched Reluctance Motor. The simulation results are presented to evaluate the performance of different concentrated winding configuration.*

Index Terms: *Configuration, Reluctance, Torque, Winding.*

I. INTRODUCTION

Switched reluctance motor (SRM) is a doubly salient structure with projecting poles on stator and rotor. The rotor has no field windings or permanent magnet. The torque is produced by the position stator and rotor poles, thereby varying the flux linkages and reluctance in the motor. These motors find applications in various domains like residential, automotive, renewable energy, aerospace, transportation and domestic appliances. Thus, continuous improvements in terms of size, volume, weight, fault tolerance, capability, high torque and efficiency, cost effective manufacturing of motors are of current interest [1].

The typical structure of SRM has salient poles on stator and rotor, which has poles, but without field windings on the rotor. The windings are located on stator poles. The generation of heat in the rotor is ruled out as there is no winding. The torque is produced by the position of stator and rotor in the machine, where the flux generated by the current carrying coil is maximized. The performance of the SRM machine can be varied when accompanied with power electronics [2].

The different winding configurations of the 12/8 SRM motor without any modification in the motor structure has been observed for different excitations and also different rotor structure. The doubly salient synchronous reluctance machine with double layer concentrated winding arrangement produces the maximum average torque at peak currents and obtain the maximum efficiency compared to conventional SRM with flux barriers inside the rotor [3].

The SRM motor with minute magnet mass or excluding magnets is of current interest. As a consequence of recent developments in power electronics industry, the SRM have attracted as a good candidates for the utilization in electric automotives. The performances of SRM are studied by finite element analysis using ANSYS Maxwell Software. The simulation result shows the small change in configuration of the machine resulting in the improvement of magnetic properties and the energy efficiency in the generation of torque. The SRMs are capable of replacing the permanent magnet synchronous machine [4].

The high temperature superconducting (HTS) winding material of the SRM and tapering poles take part a major role in the performance of the motor. The high temperature super conductor stator windings are efficient in carrying higher current while the magnet-

ic circuit was improved by the tapering of poles. The cryogenic system of the SRM is robust and simple. This configuration has been designed to improve the torque density [5, 7].

The conventional bobbin wound copper coils are more efficient when compared with pre-compressed aluminum coils having similar stranding, due to relatively low electrical conductivity of aluminum. This results in lowering of DC winding resistance by 18%, reduction in the overall mass of motor by 10% and the raw material of the winding cost by 84%. The higher AC winding losses can be eliminated by enhancing the number of winding strands in the pre-compressed winding [6].

The iron loss, the performance on different short pitch winding arrangements and core laminations have been analyzed with different core lamination materials. The different winding arrangements and the core lamination materials for same machine are analyzed for the iron loss and torque. The lamination material plays an important role in the reduction of iron losses and short pitch winding arrangements has the lowest torque ripple [8].

In this paper the different winding configurations will be employed (SLC and DLC,) for both the SRMs and same structure. The performance of the machines is compared to modular SRMs.

II. WINDING CONFIGURATIONS AND THEIR PARAMETERS

A. WINDING CONFIGURATIONS

Figure1: Structure of SRM motor with different concentrate winding (a) SL-SRM (b) DL-SRM (c) End connections

The SLC and DLC winding have been employed with excitation for exhaustive investigation in this study. For more clarity and consistency, the structure and design features of the machines are maintain constant as reflected in table I. The design features and dimensions, such as number of stator/rotor pole, shaft outer radius, pole embrace etc., are optimized individually for given machine dimension.

TABLE- I

DESIGN FEATURES AND DIMENSIONS

Number of stator pole	6	pole embrace	0.4
Number of rotor pole	4	Number of turns per pole	142
Stator outer radius (mm)	120	Type of steel	M15-26G
Length of Air gap (mm)	0.5	Stacking factor	0.95

In an effort to observe the flux distribution by the influence of winding configurations on the 3-phase, 6/4 size SRM has been selected for the analysis. According to figure 1, the rotor is at aligned position and the phase A is provided with dc current.figure.1 (b) is the parallel combination of DLC-SRM winding

configuration, in which one coil is wound on each stator tooth and each phase has parallel combination of two concentrated coils.

In addition, the coil pitch is less than the pole pitch, it can be considered as a SL winding since each stator slot having only one coil. As a result, each phase of the SL-SRM has only one coils and coil is wound around one stator tooth.

The winding configuration of SL-SRM has the potential to produce more average torque than that of the DL-SRM without magnetic saturation. In contrast the less MMF concentration in the DL-SRM indicates more flux density and hence more sensitivity to magnetic saturation.

The difference between DLC-SRM and SLC-SRM concentrated winding results in different flux paths and magnetic polarities with conventional windings wherein there is no mutual flux between phases. The flux of phase A links to phases B and C. As a result mutual flux exists and this could potentially contribute to higher on-load torque if appropriate excitation is selected. Influence of winding configurations on inductances and phase resistances.

Figure 2a: Flux linkage of Single layer of switched reluctance motor

Figure2b; Flux linkage of double layer of switched Reluctance motor.

B. PHASE RESISTANCES

The mean length per turn of the conductor decides the phase resistance value. The DL-SRM consists of two conductors in stator slot and SL-SRM consists of one conductor in stator slot. The Figure 1(c) shows different winding configurations of end connections. For a given dimension of a machine, the end-winding is affected by the configurations of winding and estimates the copper loss.

The average resistance value of end-winding length of the DL-SRMs and SL-SRMs are summarized as shown in the TABLE-2, where W_t is stator tooth width. And Ws is average stator slot width.

TABLE-II

END- WINDING CONFIGURATIONS AND WINDING RESISTANCE PER PHASE WITH COIL TEMPERATURE @ 70°C

Winding Configurations	End –windings	winding resistance per phase (Ω)
DL	$\frac{1}{4}\pi Ws + Wt$	0.727
SL	$\frac{1}{2}\pi Ws + Wt$	2.909

III SIMULATION RESULTS

A. Torque characteristics

The figure 3a describes the Torque versus speed characteristics of SLC-SRM and DLC-SRM concentrated winding configurations. The rated torque of SLC-SRM winding configuration is more compared to DLC-SRM winding configuration.

Figure3a: Torque v/s speed characteristics of SLC-SRM and DLC-SRM

B Performance of efficiency

The figure 3b describes plot of efficiency versus speed for the two different concentrated winding configurations namely SLC-SRM and DLC-SRM, the DLC-SRM winding configuration producing much higher efficiency compared to other. The efficiency gradually increases at lower speed and reaches maximum and remains constant for high speed operation of motor.

Figure3b: Efficiency v/s speed characteristics of SLC-SRM and DLC-SRM

C Output power performance

The figure 3c describes the characteristics of Output power versus speed for SLC-SRM and DLC-SRM winding configurations. The output power of DLC-SRM winding configuration is more compared with other winding configurations

Figure3c: Output power v/s speed characteristics of SLC-SRM and DLC-SRM

D Air Gap Inductance Performance

The figure 3d describes the performance of air gap inductance for two different concentrated winding configurations namely SLC-SRM and DLC-SRM, the SLC-SRM winding configuration of 6/4 SRM machine induces more inductance compared to same type of other winding configuration machine.

Figure3d: Air gap inductance v/s Electrical angle characteristics of SLC-SRM and DLC-SRM

IV CONCLUSION

This article summarizes the current developments in switched reluctance motors. The explored machines have different concentrated winding configurations (DLC and SLC) comparing the torque, efficiency and output power for various winding configuration of 6/4 SRM motor.

Regarding the concentrated winding arrangement, it was demonstrated that the modified DLC has the highest efficiency and starting torque. The maximum output power is more in DLC-SRM motor. Simulation result shows, the DLC-SRM winding configuration results are better than other winding configuration. FEA tool is used to accurately determine the motor parameters and performance predictions are simulated in ANSYS software. The total losses in the DLC-SRM are less compared with SLC-SRM machine.

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This is to certify that the paper entitled Short – term load forecasting for a grid connected microgrid presented by Mrs. Harini Vaikund of Dr. Ambedkar Institute of technology has been selected as the Best paper in the Category AI and ML in the Fourth National Conference (Online) on "Emerging Trends in Engineering, Science & Technology" held on July 05, 2021, Organized by the Department of Electronics & Communication Engineering, in collaboration with IETE.

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SHORT-TERM LOAD FORECASTING FOR A GRID CONNECTED MICROGRID

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***Abstract*—Microgrids which are widely used in present date due to their capability of operating in grid-connected and stand-alone modes and they also precisely handle the transition between the two, but microgrids are becoming more complicated, as they come with lots of regulatory issues, which becomes more difficult to comply with. To overcome this issue, load forecasting model can be developed which plays an important role in providing efficient energy. Load forecasting is a technique to estimate the energy needed to balance the supply and demand. Electrical load forecasting is an important process that can increase the efficiency and can be economically beneficial for the electrical generating and distribution companies. In this project we have done short term load forecasting for two weeks using MATLAB software tools such as Neural Net Fitting Tool and Regression Learner tool, then their results are simulated and analyzed. Further per day analysis and weekly analysis is done to get better load forecasting results to attain efficient energy system.**

Keywords—Load forecasting, Short-term forecasting, ANN, Regression Learner.

I. INTRODUCTION

Microgrid is a group of inter-connected loads and distributed energy resources within specified electrical boundaries that act as a single controllable establishment with respect to the grid. A microgrid can be operated in both connected and island mode. For microgrid to be more economical and reliable it involves the operation of solving uncertainties. Load demand and weather conditions are two such irregularities that make this coordination more challenging in isolated microgrids. With increase in population, microgrids are extensively used but they have their own challenges such as managing various types of DER's and load management [5]. This over critical demand-supply balance and higher component failure rates require solving these problems, thus demand-supply balance can be achieved using load forecasting techniques. Load forecasting is a method used to predict the power or energy needed to balance the supply the load demand in advance. Load forecasting helps an electric utility firms to make important decisions which includes decisions on purchasing and generating electric power, load switching, and infrastructure development. Load forecasting is of three types, Short-term forecasting: which is generally from one hour to one week. Medium forecasting: which ranges from a week to a year. Long-term forecasting is used for a period longer than a year. Short term load forecasting help us to estimate load demand and decisions are made that can avoid overloading [2]. There are various optimization techniques, which help us in load forecasting to get more accurate results. The methods/techniques used in our project are Artificial Neural Network and Regression method.

A. Optimizing Techniques

There are various optimization techniques which will help us to build load forecasting models. Each of these optimization techniques have their own algorithms which gives accurate predicted results. Optimization techniques of load forecasting are classified as: ANN, Regression method, Time series, Expert systems, Support vector machines and Fuzzy logic. In this paper we have done short-term load forecasting with the help of two optimization techniques which are Artificial Neural Network (ANN) and Regression Learner.

II. METHODOLOGY

- Input data is collected from weather department and output data is collected from load center [1].
- Our data contains different variables, which are temperature, relative humidity, wind speed, wind direction, hour of the day, day of week, previous day same time load, previous week same time load and whether that day is holiday or not and the output data variable is load consumption data.
- All this data is converted into matrix form and then imported to MATLAB. Data is divided into two forms input and output matrix.
- Input matrix consists of all the variables except output data. Output matrix consists of output data.

- Forecasting model is developed using these input and output matrix with the help of MATLAB tools Neural Net Fitting and Regression Learner tool.
- The test data is simulated with forecasting model to get the predicted values from model.
- The predicted values and real values are compared and analyzed. If errors are more, then model is improved with some specifications.
- Per day and weekly analysis is done to get accurate forecasting model.
- Two different models are developed with two techniques mentioned above. These models are compared with each other to choose the accurate model and the more accurate model can be used for forecasting.

Equations

An example of a neuron showing the input (x_1 - x_n), their corresponding weights (w_1 - w_n), a bias (b) and the activation function f applied to the weighted sum of the inputs.

Equation used for ANN

$$Y_i = \beta_0 + \beta_1 X_{i1} + \beta_2 X_{i2} + \ldots + \beta_{p-1} X_{i,p-1} + \varepsilon_i$$

- **Y_i is the value of the response variable for the ith case**
- **β_0 is the intercept**
- **$\beta_1, \beta_2, \ldots, \beta_{p-1}$ are the regression coefficients for the explanatory variables**

Equation used for Regression Learner

A. Simulation results and analysis

Simulation results of load forecasting were found respectively by using Neural Net Fitting tool and Regression Learner tool in MATLAB. Then comparison between these two techniques is done to find, which tool is best for short-term load forecasting

a) Simulation results of ANN tool: By using Neural Net Fitting tool simulation results are found for short-term load forecasting for two weeks [3], [4]. Further graph is plotted to compare the actual load data and load predicted by Neural Net Fitting tool. In the graph x-axis depicts time (hours) and y-axis depicts load (KW).

Fig. a. Comparison between actual and Neural Net Fitting tool predicted data.

The MAPE (mean absolute percentage error) is calculated using the below given formula)

```
>> mape = mean(abs(r./testoutput)*100)

mape =

    4.1892
```

The MAPE value of Neural Net Fitting tools was found out to be 4.182%.

b)Simulation results of Regression Learner tool: By using Regression Learner tool simulation results are found for short-term load forecasting for two weeks. Further graph is plotted to compare the actual load data and load predicted by Regression Learner tool. In the graph x-axis depicts time (hours) and y-axis depicts load (KW).

Fig. b. Comparison between actual and Regression Learner tool predicted data.

The MAPE (mean absolute percentage error) is calculated using the below given formula.

```
>> mape = mean(abs(r./testoutput)*100)

mape =

    4.2210
```

The MAPE value of Regression Learner tool was found out to be 4.2210%.
We obtained MAPE errors for both the tools, which did not exceed 5%.

c) Comparison between tools: By using Neural Net Fitting and Regression Learner tool simulation results are found of short-term load forecasting for two weeks. Further graph is plotted to compare the actual load data, load predicted by Neural Net Fitting and Regression Learner tool. In the graph x-axis represents time (hours) and y-axis represents load (KW).

Fig.c Comparison between actual load data and tools

MAPE(mean absolute percentage error) of both the tools are tabulated below:

MATLAB Tool	MAPE (%)
Neural Net Fitting	4.1892
Regression Learner	4.2210

As seen from the above table the MAPE of Neural Net Fitting tool is 4.1892% and of Regression learner is 4.2210%, MAPE of Neural Net Fitting tool is less than that of Regression Learner tool. Hence from the above plotted graphs and calculated results, we can say that Neural Net Fitting tool gives us better forecasting results when compared to Regression Learner.

B. Analysis graphs

a) Per day analysis by ANN: By using Neural Net Fitting tool graph is plotted for one day to analyze overshoot load and undershoot load occurring in a day. In the below fig. a x-axis represents hour of the day and the y-axis represents load in KW.

Fig. a Graph of one day analysis by ANN.

When referred to the above fig a, maximum load demand was predicted to be at approximately 16:30 hrs. The minimum load demand was found to be at around approximately 4:45 hrs (early morning). From this information we can balance supply and load demand for better economical operation.

b) Weekly analysis by ANN: By using Neural Net Fitting tool graph is plotted for a week to analyze overshoot load and undershoot load occurring in a day. In the below fig. b x-axis represents hour of the day and the y-axis represents load in KW.

Fig. b Graph of one week analysis by ANN.

From the above fig b, maximum load demand was found to be on Thursday (weekday).Minimum load demand was found to be on Sunday (weekend). From this information we can balance supply and load demand for better economical operation.

c) *Per day analysis by Regression Learner:* By using Regression Learner tool graph is plotted for one day to analyze overshoot load and undershoot load occurring in a day. In the below fig.c x-axis represents hour of the day and the y-axis represents load in KW. When referred to the above fig a, maximum load demand was predicted to be at approximately 16:30 hrs. The minimum load demand was found to be at around approximately 4:45 hrs (early morning). From this information we can balance supply and load demand for better economical operation.

Fig. c Graph of one day analysis by Regression Learner.

d) Weekly analysis by Regression Learner: By using Regression Learner tool graph is plotted for a week to analyze overshoot load and undershoot load occurring in a day. In the below fig. d x-axis represents hour of the day and the y-axis represents load in KW. From the above fig b, maximum load demand was found to be on Thursday (weekday).Minimum load demand was found to be on Sunday (weekend). From this information we can balance supply and load demand for better economical operation.

Fig. d Graph of one week analysis by Regression Learner.

CONCLUSION

With help of MATLAB software, we did short-term load forecasting using two tools Neural Net Fitting and Regression Learner. We have got different results for these two tools and then these results are analyzed and compared with each other to build efficient short-term forecaster. ANN technique gives us better results with minimal error percentage for short-term data. Per-day and weekly analysis are discussed which can help us in balancing the supply and demand side load. This further helps us in achieving economical beneficial power system. With intelligent techniques, load forecaster can provide uninterrupted power supply in microgrids. Due to sporadic nature of renewable resources such as wind energy or solar energy in microgrids, it is hard to obtain sustainable system thus short-term load forecasting helps the system to work efficiently. As microgrids has ability to work in isolated mode so it should be well prepared in advance. Load forecasters are flexible and useful for analyzing load growth under different situations.

When numbers of input variables are increased like number of people residing at that place and types of appliances used, then results are more precise. Usually this data is used for long-term and medium forecasters. Microgrids with good . forecasting models can be implemented in rural areas where power cut is often.

Overall short-term load forecasting maximizes usage of power generating plants. The load forecasting prevents under generation or over generation. It helps in deciding and scheduling for maintenance of the power systems. By understanding the load demand, utilities can know when to carry out maintenance and ensure that it has the minimum effects on the consumers.

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