

Dr. Ambedkar Institute of Technology
Department of Electrical and Electronics Engineering

The NAAC documents enclosed are verified and approved.

Jagadeeswari G
HOD
Department of Electrical and Electronics Engg.
Dr. Ambedkar Institute of Technology
Bengaluru-560056
5/11/22



Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU- 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi)

Department of Electrical & Electronics Engineering

MINUTES OF THE BOARD OF STUDIES MEETING

DATED: Saturday, October 16, 2021



Department of Electrical & Electronics Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 12th additional Board of Studies (BOS) meeting of the Electrical and Electronics Engineering Department was held on Saturday, the 16th October 2021 at 11:00 AM through ONLINE MODE via Google Meet at EEE Department under the Chairmanship of Dr. G.V. Jayaramaiah, Professor and Head of the Department of Electrical and Electronics Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of the BOS meeting through ONLINE MODE and gave a preliminary presentation on the agenda of approval of the schemes and first year syllabus of UG for the academic year 2021-25 under NEP-2020.

The chairman along with the BOS coordinator(s) gave a detailed presentation of the schemes semester wise and first year Basic Electrical Engineering Syllabus of both theory and laboratory of UG for the academic year 2021-22 under NEP-2020.

The following agenda was placed by the Chairman which was discussed and resolved as follows:

Agenda 1: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG for the Batch 2021-25 under Choice Based Credit System and Outcome Based Education System.

The Chairman appreciated the members about the introduction of Choice Based Credit System and Outcome Based Education System for a 4-year course in Electrical and Electronics Engineering with the course Matrix.

Accordingly, Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length, the various aspects of the Curriculum Design. After incorporating, the changes suggested by the members of BOS, the Curriculum Design for I to VIII Semesters were approved for the AY 2021-25.

Agenda 2: To frame, discuss and approve the Scheme for III and VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-25.

The BOS Chairman presented the Scheme for the III to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-25 and placed before the Board Of Studies Members for their opinion and approval.



Department of Electrical & Electronics Engineering

Resolution: The BOS members went through the Schemes thoroughly for the III to VIII Semester and discussed in length, the various aspects of the scheme. BOS members were approved the same.

Agenda 3: To discuss and approve the first year Basic Electrical Engineering Syllabus of both theory and laboratory of UG for the academic year 2021-22 under NEP-2020.

The BOS Chairman presented the first year Basic Electrical Syllabus of both theory and laboratory of UG for the academic year 2021-22 under NEP-2020.

Resolution: The BOS members went through the the first year Basic Electrical Engineering Syllabus of both theory and laboratory of UG for the academic year 2021-22 under NEP-2020 and discussed in length, the various aspects of the syllabus.

Finally, the meeting of the Board of studies was concluded with a Vote of Thanks by the Chairman of the BOS and BOS Coordinator(s) to all the members (Internal and External) of the BOS and Co-opted members for their active participation in the deliberations of the meeting and giving their inputs for the progress of the departmental academics.

Comments and Suggestions of BOS members and modifications:

1. Dr. T S Prasanna, Professor, Chairman of Electrical Engineering, UVCE, Bengaluru was suggested to include the DC machines in the curriculum as Electrical Vehicles using DC motors as drives.

Resolution : As per his comments and other members suggestions the topic is included in the syllabus.

2. Dr L. Sanjeev Kumar, Professor and Head, Department of Electrical and Electronics Engineering, SSIT, Tumakuru and VTU nominee was suggested not to burden by including the power generation as they study in higher semester.

Resolution : As per his comments and other members suggestion the topic was removed.

3. Dr. Shivananda D.V, CEO, Universal Power Controls, Tumakuru & Mr. Ramachandra, Green Automation, Bengaluru, Industry representatives were suggested to include topics on switchgears and protection.

Resolution: After discussion it was decided that a separate course on switchgears and protection will be offered to the students of 2021 Batch in the higher semesters . Hence in first year the topic is limited for Fuses & MCBs.

4. Dr. Pradip kumar Dixit, Professor and Head, Department of Electrical and Electronics Engineering, MSRIT, Bengaluru and Dr. T S Prasanna were suggested that the topic on batteries



Department of Electrical & Electronics Engineering

used in EVs can be removed as they studied in PUC level and in first year Engineering Chemistry also.

Resolution : As per their comments and other members suggestion the topic has been removed.

5. The syllabus was finalised after detailed discussions and suggestions by the expert panel members keeping in mind the following aspects
- Course is only for First year Engineering level
 - It is common to all branches
 - Not to deviate much from revised VTU framed first year syllabus under NEP-2020 (to facilitate the students who take change of college after the first year).

The following BOS members were present for the BOS meeting on Saturday, the 16th of October 2021.

1. Dr. G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr L.Sanjeeva Kumar, Professor and Head, Sri Siddhartha Institute of Technology, Tumakuru ,VTU Nominee
3. Dr. Suryanarayanadoolla, Professor in Energy Science and Engineering, IIT Bombay, Subject Expert.
4. Dr. Pradeep Kumar Dixit, Professor and Head, Electrical Engineering, MSRIT, Bengaluru, Subject Expert.
5. Dr. Prasanna.T.S., Professor and Chairman, Department of Electrical Engineering, UVCE, Bengaluru, Subject Expert.
6. Dr. Ankit Kumar Srivastava, Dept.of EEE, IET, Ayodhya UP., Invitee, Mentee Institute
7. Mr. Prakash, Executive Engineer, Karnataka Power Corporation LTD, Bengaluru, Post Graduate Alumnus Student Nominee
8. Mr Ramachandra, Green Automation Pvt. Ltd, Bengaluru, Industry Representative
9. Mr. D.V.Shivananda, CEO, Universal power Controls, Tumakuru, Industry Representative
10. Mr. Guruswamy GM, IE Power technology, Bengaluru, Industry Representative
11. Dr. Eranna, Associate Professor and BOS Member
12. Dr. Jyoti P Koujalagi, Professor and BOS Member
13. Mr. T.B. Dayananda, Associate Professor, BOS Member
14. Dr. H.V . Govindaraju, Associate Professor, BOS Member
15. Dr. S.Vasudevamurthy, Associate Professor, BOS Member
16. Ms. Nalini. S, Associate Professor, BOS Member
17. Dr. Shankaralingappa. C.B, Professor, BOS Member
18. Ms. Arpitha Raju, Assistant Professor, BOS Member



Department of Electrical & Electronics Engineering

Members Absent:

1. Dr.Udaya Kumar, Professor and Chairman, Electrical Engineering, IISc, Bengaluru, Subject Expert

Signature of Coordinators

1. Mr T.B. Dayananda

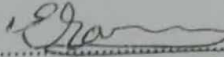
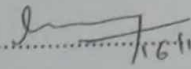
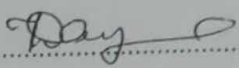
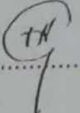
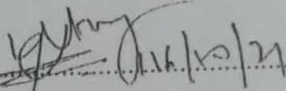
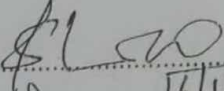
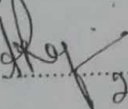
2. Mrs Harini Vaikund

Signature of the BOS Chairman

Dr G.V. Jayaramaiah



Department of Electrical & Electronics Engineering

Name of the BOS Member	Signature of the BOS Member with date
1. Dr Eranna, Associate Professor	 16/10/2021
2. Dr Jyoti P Koujalagi, Professor	 16/10/21
3. Mr T.B Dayananda	 16/10/21
4. Dr H.V Govindaraju, Associate Professor	 16/10/21
5. Dr S Vasudevamurthy, Associate Professor	 16/10/21
6. Ms Nalini. S, Associate Professor	Nalini. S
7. Dr Shankarlingappa. C.B., Professor	 16/10/21
8. Ms Arpitha Raju, Assistant Professor	 21/10/21



Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU- 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi)

Department of Electrical & Electronics Engineering

MINUTES OF THE BOARD OF STUDIES MEETING

DATED: Saturday, June 26, 2021



Department of Electrical & Electronics Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 12th Board of Studies (BOS) meeting of the Electrical and Electronics Engineering Department was held on Saturday, the 26th June 2021 at 11:00 AM through ONLINE MODE via Google Meet at EEE Department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electrical and Electronics Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of the BOS meeting through ONLINE MODE and gave a preliminary presentation on the agenda of approval of the scheme and syllabus of UG and PG for the academic year 2021-22

The chairman along with the BOS coordinator(s) gave a detailed presentation of the courses to be offered semester-wise: Professional core, Professional Elective, and Open Elective subjects to the UG level students. Also, the members were briefed about the Curriculum Design of the Department for the UG and PG courses.

The following agenda was placed by the Chairman which was discussed and resolved as follows:

Agenda 1: The confirmation of the proceedings of the 11th BOS meeting held on 08-08-2020

The 11th BOS meeting proceedings were confirmed.

Discussion: The members appreciated the progress during the academic year 2020-21.

Agenda 2: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG for the Batch 2018-21 under Choice Based Credit System and Outcome Based Education System.

The Chairman appreciated the members about the introduction of Choice Based Credit System and Outcome Based Education System for a 4-year course in Electrical and Electronics Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length, the various aspects of the Curriculum Design. After incorporating, the changes suggested by the members of BOS, the Curriculum Design for I to VIII Semesters were approved for the AY 2018-21.



Department of Electrical & Electronics Engineering

The BOS Chairman presented the Scheme and Syllabus for the III and IV Semester PG under Choice Based Credit System and Outcome Based Education System for the academic year 2021-22 and placed it before the Board Of Studies Members for their opinion and approval

Resolution: The BOS members went through the draft Scheme and Syllabus for the III and IV Semester PG applicable to the 2020-2022 Batch and discussed in length, the various aspects of the scheme and syllabus of theory. The BOS committee has also approved the Scheme and Syllabus for the AY 2020-21.

Agenda 07: Inclusion of Tools and Methods for ONLINE or Blended learning for the AY 2021-22

The External BOS members suggested introducing, the Tools and Methods for ONLINE or Blended learning to UG and PG in Electrical and Electronics Engineering for the academic year 2021-22.

Resolution: The members of the BOS approved the Tools for ONLINE/FLIPPED LEARNING ENVIRONMENT such as MICROSOFT TEAM, ZOOM, EDMODO, GOOGLE CLASSROOM, EPILOGUE, CANVAS and CISCO WEBEX MEETING and also the members of BOS suggested using software like Sequel, Scilab, PSIM, Matlab, MiPower, PSCAD, Ansys, Open RTL for simulations for the academic year 2021-22 onwards. Also discussed in length about various aspects of blended learning. External BOS members Expressed and appreciated the inclusion of blended learning for the UG and PG courses

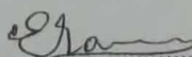
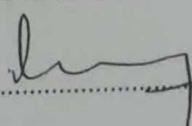
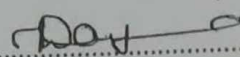
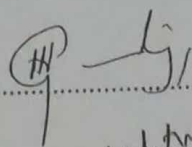
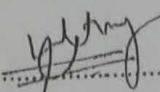
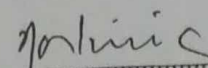
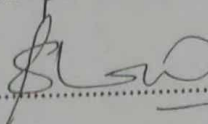
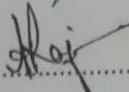
Finally, the meeting of the Board of studies was concluded with a Vote of Thanks by the Chairman of the BOS and BOS Coordinator(s) to all the members (Internal and External) of the BOS and Co-opted members for their active participation in the deliberations of the meeting and giving their inputs for the progress of the departmental academics.

The following BOS members were presented for the BOS meeting on Saturday, the 26th of June 2021.

1. Dr. G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr. Sumathi, Professor and HOD, EEE, RNSIT, Bengaluru, VTU Nominee
3. Dr. Suryanarayanadoolla, Professor in Energy Science and Engineering, IIT Bombay, Subject Expert
4. Dr. Pradeep Kumar Dixit, Professor, and Head, Electrical Engineering, MSRIT, Bengaluru, Subject Expert.
5. Dr. P.S. Puttaswamy, Professor and Head, Department of EEE, GSS, Mysore, Subject Expert.
6. Professor Dilip Kumar, HOD EEE at IET Ayodhya, UP, Invitee, Mentee Institute
7. Mr. Srinivas. B.T., Software Engineer, IBM India, Bengaluru, Post Graduate Alumnus Student Nominee



Department of Electrical & Electronics Engineering

Name of the BOS Member	Signature of the BOS Member with date
1. Dr Eranna, Associate Professor	
2. Dr Jyoti P Koujalagi, Professor	
3. Mr T.B Dayananda	
4. Dr H.V Govindaraju, Associate Professor	
5. Dr S Vasudevamurthy, Associate Professor	
6. Ms Nalini. S, Associate Professor	
7. Dr Shankarlingappa. C.B., Professor	
8. Ms Arpitha Raju, Assistant Professor	



Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU- 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi)

Department of Electrical & Electronics Engineering

MINUTES OF THE BOARD OF STUDIES MEETING

DATED: Saturday, August 08, 2020



Department of Electrical & Electronics Engineering

Ref.No: AIT/EEE/BOS / /2020-21

Date: 08-08-2020

To
Dean (Academics)
Dr Ambedkar Institute of Technology
Bengaluru-560 056

Sir

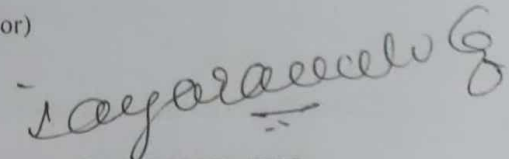
Sub: Regarding the details of the BOS meeting held on 08-08-2020
Ref: No Dr.AIT/Dean/

The BOS meeting of the EEE department was held, on Saturday, the 8th August 2020 at A009 in EEE Department.

The BOS committee has approved the following:

1. The Curriculum Design for all the semesters (III to VIII semesters) of UG Course for the AY 2020-21.
2. The Syllabus of Basic Electrical Engineering and Basic Electrical Engineering Lab for the I/II Semester for the AY 2020-21.
3. The Scheme and Syllabus for the semester III to semester VIII for the for the AY 2020-21.
4. The List of BOE members for the AY 2020-21
5. The list of Valuers / Examiners for the AY 2020-21
6. The subject Equivalence for the AY 2020-21
7. List of Subjects identified for the Honorary Degree (Major and Minor)

Thanking you


CHAIRMAN BOS
Dept. of EEE

Enclosures:

1. List of approved BOS Members for the AY 2019-21.
2. Curriculum Design for the AY 2020-21(Batch 2017, Batch 2018)
3. Minutes of the BOS Meeting for the AY 2020-21.
4. Syllabus copy of I/II Semester Basic Electrical Engineering and Electrical Engineering Lab for the academic year 2020-21.
5. Scheme and Syllabus of 3rd to 8th Semesters B.E (E & EE) for the AY 2020-21.
6. Syllabus of Interdepartmental Electives/Open Electives for the AY 2020-21.
7. List of BOE Members for the AY 2020-21.
8. List of Valuers / Examiners for the AY 2020-21.
9. List of Subject Equivalence for the AY 2020-21.
10. List of Subjects identified for the Honorary Degree(Major and Minor).



Department of Electrical & Electronics Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 10th Board of Studies (BOS) meeting of Electrical and Electronics Engineering Department was held on Saturday, August 08, 2020 at 10:30 a.m. at EEE department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electrical and Electronics Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting through ONLINE and gave a preliminary presentation on the agenda with reference to the scheme and syllabus of UG and PG for the academic year 2020-21

The chairman along with BOS coordinator(s) gave a detailed presentation of the courses to be offered semester wise, core elective subjects and Honors degree subjects to the UG level students. Also, the members were briefed about the Curriculum Design of the Department for the UG and PG Course.

The following agenda were placed by the Chairman which were discussed and resolved as follows:

Agenda 1: The confirmation of the proceedings of 9th BOS meeting held on 25-06-2018 at 10:30 am

The 9th BOS meeting proceedings were confirmed.

Discussion: The members appreciated the progress during the academic year 2019-20.

Agenda 2: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG for the academic year 2020-21 under Choice Based Credit System and Outcome Based Education System.

The Chairman appreciated the members about the introduction of Choice Based Credit System and Outcome Based Education System for 4 year course in Electrical and Electronics Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length about various aspects of the Curriculum Design. After incorporating, the changes suggested by the members of BOS, the Curriculum Design for I to VIII Semesters were approved for the AY 2020-21.



Department of Electrical & Electronics Engineering

Agenda 3: To frame, discuss and approve the syllabus of Basic Electrical Engineering and Basic Electrical Lab for the I/II Semester for the academic year 2020-21.

The Chairman presented the Syllabus of the Basic Electrical Engineering and Basic Electrical Lab for the I/II Semester students of all the branches of Engineering for the academic year 2020-21. Accordingly, a syllabus for the I/II Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Syllabus of Basic Electrical Engineering and Basic Electrical Lab is discussed in length about the various aspects of the syllabus. After incorporation of the changes as suggested by the members of BOS, the **Basic Electrical Engineering and Basic Electrical Lab syllabus** for I/II Semesters was approved for the academic year 2020-21.

Agenda 4: To frame, discuss and approve the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21.

The BOS Chairman presented the Scheme and Syllabus for the III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21 and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes thoroughly for the III to IV Semester and discussed in length about various aspects of the scheme and syllabus. BoS members were suggested to incorporate the syllabus for virtual lab, inclusion of open source/Licenced software tools for the theory/laboratory. They also suggested to change in the syllabus with the mapping of Programme specific outcomes and course outcomes, the BOS members were approved the **Scheme and syllabus** for the **III and IV Semesters** for the academic year 2020-21 under Outcome Based Education System and Choice Based Credit System.

Agenda 5: To frame, discuss and approve the Scheme and Syllabus for V to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21 and syllabus of the Institute electives offered to the other department students of the college. The List of subjects for the Honours degree course were also placed before the committee members and also the rules and regulations of the Honour's degree were placed



Department of Electrical & Electronics Engineering

before the committee members.

The BOS Chairman presented the Scheme and Syllabus for V to VIII Semester for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21 and the list of subjects for the Honours degree from V semester onwards were placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes and Syllabus thoroughly for the V to VIII Semester applicable to 2018-2019 Batch and 2017-18 Batch respectively and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the following subjects-Smart Grid Technology, and Electrical Vehicle subjects as suggested by the committee members of BOS, the **Scheme and Syllabus** for V to VIII Semesters was approved for the academic year 2020-21. The BOS committee have also approved the syllabus of the Institute/Open electives for the academic year 2020-21. The BOS members were approved the inclusion of subjects for the Honours degree courses from the V semester onwards as per the regulations laid down by the Dr AIT and VTU

Agenda 6: To prepare the panel for the Board of Examiners of the Electrical and Electronics Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2020-21

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2020-21

Resolution: The list of the Board of Examiners were approved for the AY 2020-21.

Agenda 7: To prepare the panel of Board of examiners for the evaluation and scrutinizing the Question papers in the department for the academic year 2020-21.

The Chairman of the BOS in E & EE department was presented the proposed the list panel of Examiners pertaining to UG in Electrical and Electronics Engineering department for the academic year 2020-21.

Resolution: The List of Panel of Examiners were approved based on the recommendation of internal BOS members and the Chairman of BOS for the academic year 2020-21.

Agenda 8: To frame the list of subject equivalence for the academic year 2020-21.

The Chairman of the BOS in Electrical and Electronics Engineering presented the proposed subject equivalence pertaining to UG in Electrical and Electronics Engineering for the academic year 2020-21.



Department of Electrical & Electronics Engineering

Resolution: The members of the BOS approved the subject equivalence for the students joined before the AY 2018-19, the BOS members were discussed in length about various aspects of the subject equivalence and approve the same.

Agenda 9: Inclusion of Virtual Lab for the academic year 2020-21.

The Chairman of the BOS in Electrical and Electronics Engineering presented the proposed subjects of Virtual Lab to UG in Electrical and Electronics Engineering for the academic year 2020-21.

Resolution: The members of the BOS approved the subjects of Virtual Lab for the academic year 2020-21 onwards after discussing in length about various aspects of the virtual lab. External BOS members expressed their opinion and appreciated the inclusion of the Virtual lab for UG courses

Agenda 10: Inclusion of Tools and Methods for ONLINE or Blended learning for the AY 2020-21

The External BOS members suggested to introduce, the Tools and Methods for ONLINE or Blended learning to UG in Electrical and Electronics Engineering for the academic year 2020-21.

Resolution: The members of the BOS approved the Tools for ONLINE/FLIPPED LEARNING ENVIRONMENT such as MICROSOFT TEAM, ZOOM, EDMODO, GOOGLE CLASS ROOM, EPILOGUE, CANVAS and CISCO WEBEX MEETING and also the members of BOS suggested to use the softwares like Sequel, Scilab, PSIM, Matlab, MiPower, PSCAD, Ansys, Open RTL for simulations for the academic year 2020-21 onwards. Also discussed in length about various aspects of the blended learning. External BOS members Expressed and apricated the inclusion of the blended learning for the UG courses

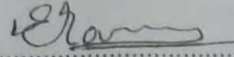
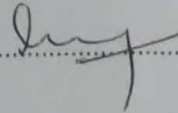
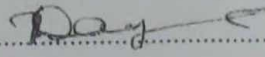
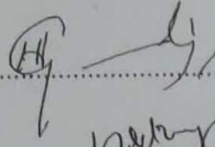
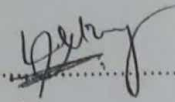
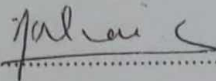
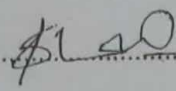
Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in Electrical and Electronics Engineering thanking to all the members (Internal and External) of the BOS and Co-opted members for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were presented for the BOS Meeting on Saturday, the 8th August 2020.

1. Dr. G.V. Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr. Sumathi, Professor and HOD, EEE, RNSIT, Bengaluru and VTU Nominee
3. Dr. Suryanarayanadoolla, Professor, Energy systems Engineering, IIT Bombay and Subject Expert
4. Dr. Pradeep Kumar Dixit, Professor and Head, Electrical Engineering, MSRIT, Bengaluru-19 and Subject Expert



Department of Electrical & Electronics Engineering

Name of the BOS Member	Signature of the BOS Member with date
1. Dr.Eranna, Associate Professor	
2. Dr.Jyoti P Koujalagi, Professor	
3. Mr.T.B.Dayananda	
4. Dr.H.V.Govindaraju, Associate Professor	
5. Dr.S.Vasudevamurthy, Associate Professor	
6. Ms.Nalini.S, Associate Professor	
7. Dr.Shankarlingappa.C.B., Professor	
8. Arpitha Raju	

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS meeting

Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. The meeting was convened on 01.06.2019 at 10.00 a.m in Research Laboratory.

To make the courses more relevant, the BOS shall incorporate necessary changes and to upgrade the courses/training programmes proposed by the Course Coordinators. In addition, BOS will also explore the possibilities for conducting few courses in collaborative manner with industries. BOS may offer suggestions for identifying thrust areas for conduction of workshop /seminar/technical seminars during 11th Plan period. A pre-BOS meeting were conducted and suggestions from Student Representatives where incorporated.

Agenda:

1. Finalizing the Scheme of UG for 2018 and 2019 batch.
2. Reviewing the Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2019 and 2020 batch.

Minutes:

- Chairperson gave introduction about the EEE Dept. and Academic Autonomy.
- The preliminary BOS meetings, along with students of UG and PG programs were put forth before the members and the relevant suggestions were included in the draft syllabus to be approved by the members, BOS.
- The members unanimously approved the following academic reforms :

RESOLUTIONS: UG

The following are the suggestions given by the BOS members,

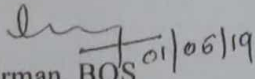
1. Dr. Puttaswamy PS suggested to retain the courses and reduce the credits keeping the contents intact at least for the 3rd and 4th semester.
2. Mr. Guruswamy expert from industry pointed out that an engineer should be able to replicate the basics what he/she has learnt effectively.
3. Focus should be good in basics or fundamental subjects for industry requirements.
4. Members mentioned to have theories and corresponding labs if any in same semester.
5. Mr. Ramachandra suggested to add **Sensors and actuators** to be one of the open elective.
6. Discussion on the curriculum design and the percentage of core and elective subjects were verified.

7. Power system planning which is at present in 6th semester should be shifted to higher semesters.
8. 3rd and 4th semester syllabus were discussed in detail.
9. Topic Saturation of Transformer to be include in the subject Transformer and Induction Machine.
10. Include self-study in all the modules of the courses; evaluation of such topics is not there for SEE.
11. Dr. Sathish and Dr. Sumathi S suggested not to have more than two books as textbooks, other books can be given as reference books.
12. Mr. Arun Kumar advised us to carry out simulation study for assignments of some subjects and the results can be submitted by the students. He also advised us to add simulation experiments along with the practical lab wherever possible.
13. The criteria for equivalence/credits for a particular subject has to be decided by the mentor coordinator with BOS coordinator & chairman.
14. The chairman of the BOS can decide as and when required the equivalent subjects to be offered to the backlog/readmission/change of college students.

RESOLUTIONS: PG

1. Mr. Guruswamy expert from industry pointed out that Snubber circuit to be included in SMPC (18EPE22) but it is already covered in PSDC (18EPE12).
2. Mr. Guruswamy also suggested that thermal design to be included in SMPC (18EPE22) but it is already covered in PSDC (18EPE12).
3. Mr. Ramachandra has suggested to incorporate Communication modules in DSP and applications.
4. Mr. Arun Kumar has insisted to include Grid connected inverters in uninterruptible power supply.
5. Dr. Puttaswamy PS advised us to add advance topics in each course as self-study component.
6. Mr. Arun Kumar has advised to include Programming in DSP course.

Further the members authorized the chair person to make appropriate changes whenever required. Chairperson concluded the meeting with vote of thanks.


Chairman, BOS
Dr. Jyoti P Koujalagi
Professor and Head
Department of Electrical Electronics Engg.
Dr. AIT, Bengaluru



DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BENGALURU
(An Autonomous Institution Affiliated to VTU, Belgaum)
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOARD OF STUDIES-EEE MEMBERS ATTENDED

Date: 1st June 2019

Sl.No	Name	Signature
1.	Dr. Jyoti P Koujalagi, Chair Person, BOE, EEE	
2.	Dr. Suryanarayana doolla, Professor, IIT Mumbai	
3.	Dr. Satish-IISc, Professor, Bengaluru	
4.	Dr. Pradip Kumar Dixit, Professor, MSRIT, Bengaluru	
5.	Dr. Puttaswamy PS, Professor, PESCE, Mandya	
6.	Dr. Sumathi, Professor, RNSIT, Bengaluru	
7.	Mr. Ramachandra, MD, Green Automation, Bengaluru	
8.	Mr. Arun Kumar, Director, Electrono Solutions, Bengaluru	
9.	Mr. Guruswamy, GM, IE Power technology, Bengaluru	
10.	Mr. Srinivas BT, Software Engineer, IBM, Bengaluru	
11.	Samarendra Prathap Singh, Asst. Prof, IET, Ayodhya, UP	
12.	Dr. B.V. Sumangala, Professor, Dr. AIT	
13.	Dr. Shankarlingappa. C.B, Professor, Dr. AIT	
14.	Dr. Eranna, Associate Professor, Dr. AIT	
15.	Mr. Dayananda T.B, Associate Professor, Dr. AIT	
16.	Dr. Govindaraju H.V, Associate Professor, Dr. AIT	
17.	Dr. Vasudevamurthy S, Associate Professor, Dr. AIT	
18.	Ms. Nalini S, Associate Professor, Dr. AIT	
19.	Ms. Arpitha Raju, Assistant Professor, Dr. AIT	
20.	Mr. Keshava Murthy, Visiting Professor, Dr. AIT	
21.	Mr. Mukunda Swamy, Assistant Professor, Dr. AIT	
22.	Mrs. Harini Vaikund, Assistant Professor, Dr. AIT	
23.	Mr. Rajesh LV, Asst. Prof.	

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS meeting

Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. The meeting was convened on 19.06.2018 at 10.00 a.m in Research Laboratory.

To make the courses more relevant, the BOS shall also do necessary changes and up gradation of all the courses/training programmes proposed by the Course Co-ordinators. In addition, BOS will also explore the possibilities for conducting few courses in collaborative manner with some other organizations. BOS may offer suggestions for Identification thrust areas for conduction of workshop /seminar/technical seminars during 11th Plan period. A pre-BOS meeting was conducted with Student Representatives.

Agenda:

1. Finalizing the Scheme and Syllabus of UG for 2018-19 batch.
2. Reviewing the Scheme and Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2018-19 batch and to revise the Scheme and Syllabus of existing batches.

Minutes:

- Chairperson gave introduction about the EEE Dept. and Academic Autonomy.
- The preliminary BOS meetings, along with students of UG and PG programs. The proposals put forth by the students were examined and the relevant items were included in the draft syllabus to be approved by the members, BOS.
- The members unanimously approved the following academic reforms :

RESOLUTIONS: UG

The following changes are applicable to 2018 batch.

1. Two machine courses- Electrical Machines-I & Electrical Machines-II are mandatory for electrical course.
2. Board advised to make Modern control theory as an elective which was made as core in pre BOS meeting.
3. They advised to shift Generation, Transmission and Distribution course for higher semester.

(62)

4. Field theory course must be completed before doing Generation, Transmission and Distribution.
5. They mentioned to have theories and corresponding labs if any in same semester.
6. Management and humanitarian courses and/or credits should be reduced to include more number of core subjects.
7. They advised to reduce the credits of main project and include that in main subjects.
8. Classes not to be allotted for final year students on one or two days in a week to facilitate the students to carry out their project in industries.
9. Either the courses or credits need not be made compulsory but overall credits should be maintained as per the specified (175).
10. Operation & Research and PLC as core subjects to be included in the syllabus.

RESOLUTIONS: PG

1. Thesis to be given more weightage than internship as thesis plays a key role in placement for students unlike undergraduates.
2. In the first sem there should be an elective in place of miniproject / Industrial visit
3. Technical seminar is mandatory in the first semester only and has to be a credit course.
4. Second semester has to have a mini project which is evaluated by a committee of 4 to 5 members.
5. Final semester has to have only project and should not be clubbed with any other theory subjects.
6. Course can be clubbed with internship during third semester only.
7. Number of contact hours/week should be equal to the credits mentioned.

Further the members authorized the chair person to make appropriate changes whenever required. Chairperson concluded the meeting with vote of thanks.



Dr. Jyoti P Koujalagi
Professor and Head

Department of Electrical Electronics Engg.
Dr. AIT, Bengaluru

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS Meeting

Agenda:

1. Finalizing the Scheme and Syllabus of UG for 2017-18 batch.
2. Reviewing the Scheme and Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2017-18 batch and to revise the Scheme and Syllabus of existing batches.

The meeting was convened on 25.03.2017 at 10.00 AM in the Research Laboratory. Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. Chairman informed the members about-

- Preliminary BOS meetings were conducted on 15th, 18th & 20th March 2017, with student members of UG and PG programs and the course co-ordinators.
- A draft copy of scheme and syllabus was framed by considering their suggestions to make the courses more relevant.
- The members approved the scheme and syllabus with the following academic reforms.

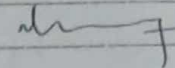
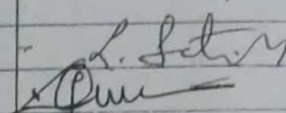
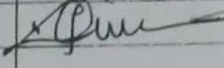
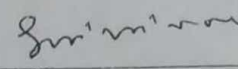
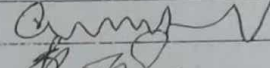
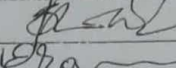
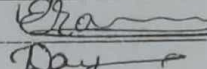
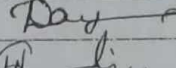
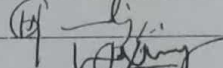
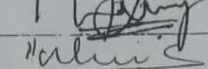
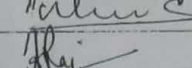
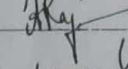
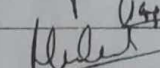
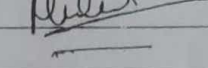
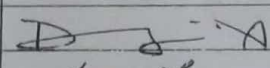
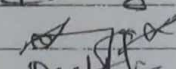
RESOLUTIONS:

1. **3rd semester:** Applicable to 2016- 2017 batch.
 - a. As per the regulation of Dr AIT (Autonomous), the total number of registered credits even after withdrawal of course shall be at least 20 (13.3.2- Academic regulation 2016-17). It was resolved to increase, the 3rd semester credits from 23 to 24, to allow the students to drop / withdraw any course. This has been resolved by increasing the credits of the core subject **EE32 Logic Design** from 3 to 4.
 - b. **The contents of the core subject EE32 Logic Design were revised.**

DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BENGALURU
(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
LIST OF BOARD OF STUDIES EEE MEMBERS FOR THE YEAR 2017-2019

Attendance List

Sl. No.	Names	Signatures
1	Dr. Jyoti P Koujalagi, Chairman	
2	Dr. Suryanarayana doolla, IIT, Mumbai	
3	Dr. Satish L, IISc, Bengaluru	
4	Dr. Pradeep Kumar Dixit, MSRIT, Bengaluru	
5	Dr. Saikumar-NIE, Mysuru	
6	Dr. Ravishankar Dixit , BMSEE , -VTU Nominee	
7	Dr. Balaram, PRDC, Bengaluru	
8	Mr. Chetan Rajdev, Deputy GM, Bosch Rexorth, Bengaluru	
9	Mr. Srikanth Kashyap, Director, JVS Electronics, Bengaluru	
10	Mr. Srinivas BT, Software Engineer, IBM India Pvt. Ltd, Bengaluru	
11	Dr. B.V. Sumangala, Professor	
12	Dr. Shankarlingappa.C.B Professor	
13	Mr. Eranna, Asso. Prof.	
14	Mr. Dayananda T.B, Asso. Prof.	
15	Mr. Govindaraju H.V, Asso. Prof.	
16	Mr. Vasudevamurthy S, Asso. Prof.	
17	Ms. Nalini S, Asso. Prof.	
18	Ms. Arpitha Raju	
19	Mr. Keshava Murthy, Visiting Professor	
20	Mr. MukundaSwamy, Asst. Prof.	
21	Mrs. Harini Vaikund, Asst. Prof.	
22	Mrs. Dhanyavathi., Asst. Prof.	
23	Mrs. Soumya S, Asst. Prof.	
24	Mrs. Deepti S.S, Asst. Prof.	