



Panchajanya Vidya Peetha Welfare Trust (Regd)

# Dr. Ambedkar Institute of Technology

An Autonomous Institution, Affiliated to Visvesvaraya Technological University, Belagavi,  
Aided by Govt. of Karnataka, Approved by All India Council for Technical Education (AICTE), New Delhi  
Accredited by NBA and NAAC with 'A' Grade

BDA Outer Ring Road, Mallathalli, Bengaluru - 560 056

Ref. No. .... Date : .....

The following list of courses have been approved by the BOS committee for revision in course content or introducing as a new course in the scheme 2018-2022.

| Subject code | Subject   | Remarks   |
|--------------|---|---|
| 18EC31       | Electronic Devices                              | Introduced in 2019  |
| 18EC32       | Digital System Design                           | Introduced in 2019  |
| 18EC33       | Network Theory                                  | Introduced in 2019  |
| 18EC34       | Engineering Statistics                          | Introduced in 2019  |
| 18ECL36      | Electronic Devices & Instrumentation Laboratory | Introduced in 2019  |
| 18ECL37      | Digital System Design Lab                       | Revised   |
| 18EC42       | Principles of Communication Systems             | Introduced in 2019  |
| 18EC43       | Computer Organization and Architecture          | Introduced in 2019  |
| 18ECL46      | Analog Circuits and Communication Laboratory    | Introduced in 2019  |
| 18EC51       | Electromagnetic Waves                           | Introduced in 2020  |
| 18EC61       | CMOS VLSI Design                                | Introduced in 2020  |
| 18EC63       | Computer Communication Network                  | Introduced in 2020  |
| 18ECL67      | VLSI Laboratory                                 | Revised and shifted from 7 <sup>th</sup> sem to 6 <sup>th</sup> sem 2020            |
| 18EC72       | Microwave and Antenna                           | Revised in 2021   |
| 18EC732      | Virtual Reality                                 | Introduced in 2021  |
| 18EC552      | Programming with Python                         | Introduced in 2020  |
| 18EC644      | System Verilog for verification                 | Introduced in 2020  |
| 18EC645      | Internet of Things                              | Introduced in 2020  |
| 18EC71       | Wireless Communication                          | Introduced in 2021  |
| 18EC755      | Introduction to Unmanned Aerial Vehicle (UAV)   | Introduced in 2021  |
| 18EC731      | 5G Technology                                   | Introduced in 2021  |
| 18EC31       | Electronics Devices                             | Revised and reduced to 3 credits from 4 in 2020                                     |
| 18EC32       | Digital Electronics                             | Revised and changed title from Digital System Design to Digital Electronics in 2020 |
| 18EC34       | Signals and Systems                             | Introduced in place of Engg. Statistics in 2020                                     |
| 18EC41       | Analog Circuits                                 | Revised and reduced to 3 credits from 4 in 2020                                     |

  
Signature of BOS Chairman

  
Signature of Principal



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Ref. No.

Date :

|         |                                      |   |
|---------|--------------------------------------|---|
| 18EC43  | Electromagnetic Waves                | Introduced in place of Computer organisation and Architecture in 2020       |
| 18EC45  | Control Systems                      | Introduced in place of Signal and Systems in 2020                           |
| 18EC54  | Object Oriented Programming with C++ | Introduced as Electromagnetic Waves was shifted to 4 <sup>th</sup> sem 2020 |
| 18EC001 | Real Time Operating System           | Introduced as IDE was offered from V sem 2020                               |
| 18EC002 | Mechatronics                         | Introduced as IDE was offered from V sem 2020                               |
| 18EC003 | Digital Television Engineering       | Introduced as IDE was offered from V sem 2020                               |
| 18EC004 | Sensors                              | Introduced as IDE was offered from V sem in 2020                            |

  
Signature of BOS Chairman

  
Signature of Principal



## **Department of Electronics & Communication Engineering**

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### **Minutes of Board of Studies (BOS) Meeting:**

The Meeting of Board of Studies (BOS) of department Electronics and Communication Engineering was held on 19-06-2021 at 11:00 a.m. under the Chairmanship of the Dr. Ramesh S, Professor and Head, Department of Electronics and Communication Engineering on Digital Platform Google Meet with meeting-id: [meet.google.com/ock-ffdx-ngs](https://meet.google.com/ock-ffdx-ngs)

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2021-22

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the students in both Core and Elective subjects in semester wise at the Under Graduate level and also briefed the members about the Curriculum Design of the Department for the UG Course.

### **PROCEEDINGS/RESOLUTIONS:**

**The following are the Suggestions of the members of BOS with reference to the presentations:**

- Inter-department Elective Subject, Unmanned Automatic Vehicles (UAV) should be introduced in the curriculum.

**Sol.** Subject 'Introduction to Unmanned Automatic Vehicles (UAV)' with subject code 18ECE13 added to Open Elective-C list.

- Syllabus of Inter-department Elective Subject, Television Engineering (18ECE03) must be updated.

**Sol.** Syllabus of Television Engineering (18ECE03) subject is updated and renamed as Digital Television Engineering.



Department of Electronics & Communication Engineering

Finally, the BOS members approved the following after incorporating the suggested modifications

- Approved the Curriculum Design for the semesters I to VIII of UG Course for the students of the Batch 2021
- Approved the Syllabus of Basic Electronics for the semesters I/II of UG Course for the academic year 2021-22.
- Approved the Scheme and syllabus for all the semesters III to VIII of UG Course for the academic year 2021-22.
- Approved the Scheme and syllabus for all the semesters V to VIII of UG Course for the academic year 2021-22.
- Approved the Scheme and syllabus for all the semesters VII to VIII of UG Course for the academic year 2021-22.
- Approved the Syllabus for Institute Electives offered by the ECE Department.
- Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.
- Approved the List of BOE members.
- Approved the list of Valuers / Examiners.

CHAIRMAN

4.8.21

BOS Dept. of ECE

HOD

Dept. of Electronics and Communication Engg.

Dr. Ambedkar Institute of Technology

Bengaluru - 560056

BOS Coordinators

Signatures

1. Dr. Shivaputra

2. Mr. Anand H D



## Department of Electronics & Communication Engineering

### Minutes of Board of Studies (BOS) Meeting:

The Meeting of Board of Studies (BOS) of department Electronics and Communication Engineering was held on 20-08-2020 at 10:30 a.m. under the Chairmanship of the Dr. Ramesh S. Professor and Head, Department of Electronics and Communication Engineering on Digital Platform Google Meet with meeting-id.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2020-21. The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the students in both Core and Elective subjects in semester wise at the Under Graduate level and also briefed the members about the Curriculum Design of the Department for the UG Course.

### PROCEEDINGS/RESOLUTIONS:

**The following are the Suggestions of the members of BOS with reference to the presentations:**

- Tutorial sessions need to be included for the courses wherever more practice is required.  
**Sol.** Tutorial Sessions incorporated for Subjects including Numerical.
- New technologies like Machine learning, Artificial Intelligence should be introduced in the curriculum.  
**Sol.** Subjects introduced
- Simulation tools relevant to the course need to be integrated.  
**Sol.** Virtual labs, Xilinx and Cadence have been included in curriculum
- Department should play crucial role in placement orientation.  
**Sol.** Placement and training as made a mandatory non-credit subject in curriculum
- Mini projects in courses: Understanding and presentation of relevant IEEE journal papers.  
**Sol.** More of IEEE journals based and Industry based projects will be implemented as Mini Projects and Major Projects.
- Assignment Programs should be added to Python Programming Course.  
**Sol.** Programming Exercises have been included in appropriate Modules.



**Department of Electronics & Communication Engineering**

Finally the BOS members approved the following after incorporating the suggested modifications

- Approved the Curriculum Design for the semesters I to VIII of UG Course for the students of the Batch 2017
- Approved the Syllabus of Basic Electronics for the semesters I/II of UG Course for the academic year 2020-21.
- Approved the Scheme and syllabus for all the semesters III to VIII of UG Course for the academic year 2020-21.
- Approved the Syllabus for Institute Electives offered by the ECE Department.
- Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.
- Approved the List of BOE members.
- Approved the list of Valuers / Examiners.

  
CHAIRMAN *ds. 8. 2020*  
BOS Dept.ECE  
HOD

Dept. of Electronics and Communication Engg.,  
Dr. Ambedkar Institute of Technology,  
Bengaluru - 560056

**BOS Coordinators**

1. Dr. Shivaputra
2. Mr. Anand H D

**Signatures**




### Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 9<sup>th</sup> Board of Studies (BOS) meeting of Electronics and Communication Engineering Department was held on 11-05-2019 at 11 a.m. at ECE department under the Chairmanship of Dr.G.V.Jayaramaiah. Professor and Head of the Department of Electronics and Communication Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2019-20

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the UG level students of both Core and Elective subjects in a semester wise and also briefed the members about the Curriculum Design of the Department for the UG Course.

The following agenda were placed by the Chairman which were discussed and resolved as follows:

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**Agenda 1:** The confirmation of the proceedings of 8<sup>th</sup> BOS meeting held on 25-06-2018 at 10 am

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The 8<sup>th</sup> BOS meeting proceedings were confirmed.

**Discussion:** The members appreciated the progress during the academic year 2018-19.

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**Agenda 2:** To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG Course for the academic year 2019-20 onwards under Choice Based Credit System and Outcome Based Education System.

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The Chairman appraised the members about the introduction of Choice Based Credit System and Outcome Based Education System for 4 year course in Electronics and Communication Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

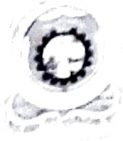
**Resolution:** The BOS members went through the Curriculum Design and discussed in length about various aspects of the Curriculum Design. After Incorporating, the changes suggested by the members of BOS, the **Curriculum Design** for I to VIII Semesters was approved 2019-20 onwards.

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**Agenda 3:** To frame, discuss and approve the syllabus of Basic Electronics for I and II Semester for the academic year 2019-20.

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The Chairman presented the Syllabus of the Basic Electronics for the First and Second Semester students of all the branches of Engineering for the academic year 2019-20. Accordingly, a draft syllabus for the First and Second Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.



**Department of Electronics & Communication Engineering**

**Resolution:** The BOS members went through the Syllabus of Basic Electronics and discussed in length about the various aspects of the syllabus. After Incorporation of the Changes as suggested by the members of BOS, the **Basic Electronics syllabus** for I and II Semesters was approved for the academic year 2019-20.

**Agenda 4:** To frame, discuss and approve the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2019-20 onwards.

The BOS Chairman presented the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2019-20 onwards and placed before the **Board Of Studies Members** for their opinion and approval.

**Resolution:** The BOS members went through the draft Schemes thoroughly for the III to VIII Semester and discussed in length about various aspects of the title of the subjects and the laboratory. After incorporating certain Changes in the title of subjects as suggested by the committee members of BOS, the **Scheme** for III and IV Semesters was approved for the academic year 2019-20 onwards under **Outcome Based Education System and Choice Based Credit System**.

**Agenda 5:** To frame, discuss and approve the Scheme and Syllabus for V to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2019-20 onwards and syllabus of the Institute electives offered by the other department students of the college.

The BOS Chairman presented the Scheme and Syllabus for V to VIII Semester for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 and placed before the **Board Of Studies Members** for their opinion and approval.

**Resolution:** The BOS members went through the draft Schemes and Syllabus thoroughly for the V to VIII Semester applicable to 2016-17 and Batch, 2017-18 Batch and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the certain Changes in the Computer Communication Network and Computer Communication Network Laboratory subjects as suggested by the committee members of BOS, the **Scheme and Syllabus** for III to VIII Semesters was approved for the academic year 2018. The members of the BOS committee have also approved the syllabus of the Institute electives for the academic year 2019-20.

**Agenda 6:** To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2019-20.

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2019-20.





Department of Electronics & Communication Engineering

**Resolution:** As suggested by the members of the BOS, the list of the Board of Examiners was approved.

**Agenda 7:** To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering for the academic year 2019-20.

The Chairman of the BOS in E & C department presented the proposed panel of Examiners pertaining to UG in Electronics and Communication Engineering for the academic year 2019-20.

**Resolution:** The Panel of Examiners were approved based on the recommendation of BOS members including the changes which were made in the list of Examiners.

**Agenda 8:** To frame the subject equivalence for the academic year 2019-20.

The Chairman of the BOS in E & C Department presented the proposed subject equivalence pertaining to UG in Electronics and Communication Engineering for the academic year 2019-20.

**Resolution:** The members of the BOS approved the subject equivalence for the academic year 2019-20 onwards after discussing in length about various aspects of the subject equivalence.

Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in ECE Department thanking to all the members of the BOS for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were present on Saturday, June 23, 2018.

1. Dr.G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr..Sri Ram P Hegde, Professor and HOD, ECE, SDMIT, Ujier, Dakshina kannada District, Karnataka and VTU Nominee
3. Dr.Devendra Jalihal, Professor and HOD, EEE, IIT Madras and Subject Expert
4. Dr.Santanu Mahapara Professor, Electronics and System Design, IISc, Bengaluru-19
5. Professor Ramapati Mishra, Director and Professor at IET Ayodhya, UP and Subject Expert
6. Prof. P.Nagaraj., Associate Professor, ECE, RVCE, Bengaluru-59 and Subject Expert
7. Mr Kubendra, Alumni Student Nominee
8. Mr Narasimha Murthy, Project Manager, Analog Devices and Industry Representative
9. Dr.H.Umadevi, Professr, BOS Member
10. Akalpita L Kulkarni, Associate Professr, BOS Member
11. Dr.S.Ramesh, Professor, BOS Member
12. Dr.Shivaputra, Assistant Professor, BOS Member
13. Dr Jambunath S Baligar, Assistant Professor, BOS Member
14. Dr. Chetan, S, Assistant Professor, BOS Member



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**Department of Electronics & Communication Engineering**

Members Absent:

1. Dr. J. Krishna Kishore, Senior Scientist, Industry Representative
2. Arun Chandrashekar, Principal Scientist, Industry Representative
3. Dr. Mrityunjaya V Latte, Principal JSS Academy of Technical Education, Academic Expert
4. Dr. Umadevi, Professor, Internal BOS Member
5. Dr. Mahalinga V Mandi, Professor, Internal BOS Member

Signature of the Coordinator(s)

Signature of the BOS Chairman



### **Minutes of the Board of Studies (BOS) Meeting:**

The proceedings of the 8<sup>th</sup> Board of Studies (BOS) meeting of Electronics and Communication Engineering Department was held on 23-06-2018 at 09:30 a.m. at ECE department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electronics and Communication Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2018-19

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the UG level students of both Core and Elective subjects in a semester wise and also briefed the members about the Curriculum Design of the Department for the UG Course.

The following agenda were placed by the Chairman which were discussed and resolved as follows:

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**Agenda 1:** The confirmation of the proceedings of 7<sup>th</sup> BOS meeting held on 25-03-2017 at 10 am

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The 7<sup>th</sup> BOS meeting proceedings were confirmed.

**Discussion:** The members appreciated the progress during the academic year 2017-18.

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**Agenda 2:** To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG Course for the academic year 2018-19 onwards under **Choice Based Credit System** and **Outcome Based Education System**.

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The Chairman appraised the members about the introduction of **Choice Based Credit System** and **Outcome Based Education System** for 4 year course in Electronics and Communication Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the **Board Of Studies** Members for their opinion and approval.

**Resolution:** The BOS members went through the Curriculum Design and discussed in length about various aspects of the Curriculum Design. After Incorporating, the changes suggested by the members of BOS, the **Curriculum Design** for I to VIII Semesters was approved 2018-19 onwards.

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**Agenda 3:** To frame, discuss and approve the syllabus of Basic Electronics for I and II Semester for the academic year 2018-19.

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The Chairman presented the Syllabus of the Basic Electronics for the First and Second Semester students of all the branches of Engineering for the academic year 2018-19. Accordingly, a draft syllabus for the First and Second Semesters was presented and placed before the **Board Of Studies** Members for their opinion and approval.



**Department of Electronics & Communication Engineering**

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**Resolution:** The BOS members went through the Syllabus of Basic Electronics and discussed in length about the various aspects of the syllabus. After Incorporation of the Changes as suggested by the members of BOS, the **Basic Electronics syllabus** for I and II Semesters was approved for the academic year 2018-19.

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**Agenda 4:** To frame, discuss and approve the Scheme for III to VIII Semester under **Choice Based Credit System** and **Outcome Based Education System** for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards.

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The BOS Chairman presented the Scheme for III to VIII Semester under **Choice Based Credit System** and **Outcome Based Education System** for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards and placed before the **Board Of Studies Members** for their opinion and approval.

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**Resolution:** The BOS members went through the draft Schemes thoroughly for the III to VIII Semester and discussed in length about various aspects of the title of the subjects and the laboratory. After incorporating certain Changes in the title of subjects as suggested by the committee members of BOS, the **Scheme** for III to VIII Semesters was approved for the academic year 2019-20 onwards under **Outcome Based Education System** and **Choice Based Credit System**.

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**Agenda 5:** To frame, discuss and approve the Scheme and Syllabus for III to VIII Semester under **Choice Based Credit System** and **Outcome Based Education System** for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards and syllabus of the Institute electives offered by the other department students of the college.

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The BOS Chairman presented the Scheme and Syllabus for III to VIII Semester for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 and placed before the **Board Of Studies Members** for their opinion and approval.

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**Resolution:** The BOS members went through the draft Schemes and Syllabus thoroughly for the III to VIII Semester applicable to 2015-16 Batch, 2016-17 Batch and 2017-18 Batch and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the certain Changes in the Computer Communication Network and Computer Communication Network Laboratory subjects as suggested by the committee members of BOS, the **Scheme and Syllabus** for III to VIII Semesters was approved for the academic year 2018. The members of the BOS committee have also approved the syllabus of the Institute electives for the academic year 2018-19.

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**Agenda 6:** To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2018-19.

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The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2018-19.



Department of Electronics & Communication Engineering

**Resolution:** As suggested by the members of the BOS, the list of the Board of Examiners was approved.

**Agenda 7:** To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering for the academic year 2018-19.

The Chairman of the BOS in E & C department presented the proposed panel of Examiners pertaining to UG in Electronics and Communication Engineering for the academic year 2018-19.

**Resolution:** The Panel of Examiners were approved based on the recommendation of BOS members including the changes which were made in the list of Examiners.

**Agenda 8:** To frame the subject equivalence for the academic year 2018-19.

The Chairman of the BOS in E & C Department presented the proposed subject equivalence pertaining to UG in Electronics and Communication Engineering for the academic year 2018-19.

**Resolution:** The members of the BOS approved the subject equivalence for the academic year 2018-19 onwards after discussing in length about various aspects of the subject equivalence.

Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in ECE Department thanking to all the members of the BOS for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were present on Saturday, June 23, 2018.

1. Dr.G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr.Shanthala, Professor and HOD, TCE, BIT, Bengaluru-4 and VTU Nominee
3. Dr.Sadashivappa, Professor and HOD, TCE, RVCE, Bengaluru and Subject Expert
4. Dr.Sheshachalam, Professor, ECE, BMSCE, Bengaluru-18 and Subject Expert
5. Dr.Raveesh Aradhy, Professor, ECE, RVCE, Bengaluru-59 and Subject Expert
6. Dr.Amit Jain, Associate Professor, ECE, CMRIT, Bengaluru-37 and Subject Expert
7. Premakumar Melukote, Alumni Student Nominee
8. Srinivasa.R.K, Project Manager, Qualcomm India Pvt.Ltd, Industry Representative
9. Vinay Sidhaya, Principal Hardware Engineer, Westen Digital, Industry Representative
10. Dr.H.Umadevi, Professr, BOS Member
11. Akalpita L Kulkarni, Associate Professr, BOS Member
12. Dr.S.Ramesh, Professor, BOS Member
13. Dr.S Shivaputra, Assistant Professor, BOS Member
14. Meenakshi L Rathod, Assistant Professor, BOS Member



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Department of Electronics & Communication Engineering

Members Absent:

1. Girish Chandra, TCS, Industry Representative
2. Dr. Debabrata Das, Dean-Academics and R & D, Academic Expert

Signature of the Coordinator(s)

Signature of the BOS Chairman

1. Dr. S. Shanthala  
VTU Nominee

2. Dr. Seshachalam D

3. Dr. Sadasivappa. G

4. Dr. Raveesh Aaradya H.V

**Department of Electronics & Communication Engineering**

**Minutes of Board of Studies (BOS) Meeting:**

The Meeting of Board of Studies (BOS) of department Electronics and Communication Engineering was held on 25-03-2017 at 10:00 a.m. under the Chairmanship of the Dr. Rajanna.K.M, Associate Professor and Head, Department of Electronics and Communication Engineering in VLSI lab Hall of Electronics and Communication Engineering Department.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2017-18

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the students in both Core and Elective subjects in semester wise at the Under Graduate level and also briefed the members about the Curriculum Design of the Department for the UG Course.

**PROCEEDINGS/RESOLUTIONS:**

**The following are the Suggestions of the members of BOS with reference to the presentations:**

- Power Electronics should be made as a core subject.
- Computer communication network should be in 6<sup>th</sup> semester and information theory and coding should be in 7th semester
- Include Mobile communication subject in 8th semester and update syllabus
- Data Analysis subjects should be in Electives
- Combine similar experiments and maximum experiments should be only 10 in lab syllabus
- Self study to be included in all the semesters and should meet required cos and pros
- Cover basics of HDL(Hardwae and software) in verilog HDL syllabus
- Combine syllabus of LIC with AEC
- Include fundamentals of VLSI in Analog mixed mode VLSI syllabus
- EC62(Digital communication) Concentrate more on keying, coherent and no coherent also system design(spread spectrum)
- Operating system(OS) and Embedded system to be made as one subject
- Information Theory and coding(ITC):Concentrate more on convolution codes and fundamental codes
- Update Digital switching system syllabus
- Object Oriented Program and Data Structure to be made as one subject.



**Department of Electronics & Communication Engineering**

Finally the BOS members approved the following after incorporating the suggested modifications

- Approved the Curriculum Design for the semesters I to VIII of UG Course for the students of the Batch 2014
- Approved the Syllabus of Basic Electronics for the semesters I/II of UG Course for the academic year 2017-18.
- Approved the Scheme and syllabus for all the semesters III to VIII of UG Course for the academic year 2017-18.
- Approved the Syllabus for Institute Electives offered by the ECE Department.
- Approved the List of BOE members.
- Approved the list of Valuers / Examiners.
- Approved the subject Equivalence.
- Approval to extend the existing Syllabus of Data structure with minor modification to the Batch-2013 such as the inclusions of numerical examples in all the units

**SUMMARY OF THE CURRICULUM DESIGN UNDER AUTONOMY:**

| <b>SL. NO.</b> | <b>Category</b>                | <b>Credits</b> |
|----------------|--------------------------------|----------------|
| 1.             | Humanities and Social Science  | 10             |
| 2.             | Basic Sciences                 | 31             |
| 3.             | Engineering Sciences           | 34             |
| 4.             | Professional Subjects-Core     | 85             |
| 5.             | Professional subjects Elective | 16             |
| 6.             | Inter Departmental Electives   | 08             |
| 7.             | Project Work/Seminar           | 16             |
|                | <b>TOTAL</b>                   | <b>200</b>     |

**CHAIRMAN**  
**BOS Dept.ECE**





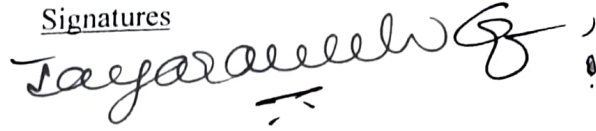
**Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.**  
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

**Department of Electronics & Communication Engineering**

BOS Coordinators

1. Dr.G.V.Jayaramaiah
2. Vijayakumar.R.H
3. Shivaputra.

Signatures





  
Chairman BOS