



Dr. S Vasudevamurthy

DEPARTMENT	Electrical and Electronics Engineering		
E- MAIL ID	Vasudevamurthy.ee@drait.edu.in		
DESIGNATION	Associate professor		
DATE OF JOINING THE INSTITUTION	27-09-1991		
QUALIFICATIONS	UG	PG	PH.D.
	B.E	M.E	PH.D
EXPERIENCE	TEACHING	RESEARCH	INDUSTRY
	31	11	0
RESEARCH AREA	Insulation and High Voltage Engineering		

RESEARCH PUBLICATIONS	NC :5	NJ :2
	IC :4	IJ :16
PH.D'S / PROJECTS GUIDED	NIL	
PROFESSIONAL MEMBERSHIPS	IEI, ISTE, ISEEE	
NUMBER OF WORKSHOPS/ CONFERENCES ATTENDED	Workshop attended - 19	Conferences attended - 15
NUMBER WORKSHOPS/ CONFERENCES ORGANIZED	10	1
BOOKS PUBLISHED / IPRS / PATENTS	2 US Patents and 2 Indian Patents in the area of liquid dielectric	
CONSULTANCY ACTIVITIES/ GRANTS RECEIVED	Insulation and HV Engg	Research Grant:19.34 Lakhs
INTERACTION WITH OUTSIDE WORLD	BOE Member in VTU and several autonomous Institutions	
ANY OTHER INFORMATION	Coordinator – ISTE students chapter of Dr.AIT Coordinator - Electrical facilities of Dr.AIT	