Dr Shilpa K C

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Address: Bengaluru ,Karnataka,India - 560040

Expertise

Electrical and Electronic Engineering

Research Area: Electronics circuits, VLSI, Artificial Intelligence

Work experience

1. Dr Ambedkar Institute of Technology, Bengaluru 2007 — Present

   Assistant Professor
   Bengaluru

Education

1. Ph.D - 2017

   BMSCE

Honours and Awards

Topper with silver medal : CMOS DIGITAL VLSI DESIGN - 2023

1.

   NPTEL

2. Identified as Shining Star, for 4 years consecutive topper in NPTEL - 2023

   NPTEL

3. Topper with silver medal : Integrated Circuits, Mosfets, OP-Amps and their Applications - 2022

   NPTEL

4. Topper with silver medal : Fundamentals of Electronic Materials and Devices - 2021

   NPTEL

5. Topper with silver medal : Microelectronics: Devices to circuits - 2020

   NPTEL

Membership In Professional Bodies

1. ISTE, 2021
   FULL TIME

2. Cryptography Society of India, 2012
   FULL TIME

3. IETE, 2007
   FULL TIME

Membership In Committees

Publication

1. Performance Analysis by Improving Energy Efficiency in IoT Based

Wireless Sensor Network for Routing Algorithm

Anand H D, Shilpa K C, Chetan S
INDIAN JOURNAL OF SCIENCE AND TECHNOLOGY, Volume Volume: 16, Issue: 11, Year 2023, Pages 785-794

2. REAL-TIME WOMEN SECURITY SYSTEM USING IOT

Shilpa K.C
Journal of Emerging Technologies and Innovative Research (JETIR) www.jetir.org, Volume VOLUME 9 ISSUE 7, Year 2022, Pages 149-151

3. Optimized Low Power Dual Edge Triggered Flip-flop with Speed Enhancement

Shilpa K.C.;Lakshminarayana C.
International Journal of Image, Graphics and Signal Processing, Volume 14, Year 2022, Pages 50-63

4. Smart Movable Road Divider, Ambulance Clearance and Intelligent Street lights using IoT

NOVIY MIR Research Journal, Year 2021

5. IOT BASED ENVIRONMENT MONITORING SYSTEM

Shilpa K.C
Journal of Emerging Technologies and Innovative Research (JETIR) www.jetir.org, Volume Volume 8, Year 2021, Pages e134-139

6. Bluetooth Technology -Architecture, Applications, and Issues: A Literature Survey

Arati S.N., Ambika B., Divya K, Gayathri A, Nidhi S
International Research Journal of Engineering and Technology (IRJET) , Volume , Year 2020, Pages

7. Optimal Resource Allocation and Binding in High-Level Synthesis Using Nature-Inspired Computation

K. C. Shilpa ., C. LakshmiNarayana ., Manoj Kumar Singh .,
Lecture Notes in Electrical Engineering, Volume 545, Year 2019, Pages 1107-1118

8. Performance Analysis of Parallel Prefix Adder for Datapath VLSI Design

Shilpa K.;Shwetha M.;Geetha B.;Lohitha D.;Navya;Pramod N.
Proceedings of the International Conference on Inventive Communication and Computational Technologies, ICICCT 2018, Volume , Year 2018, Pages 1552-1555

9. Design of CORDIC coprocessor for sparc V8

Nirmala Y.N.;Shilpa K.C.;Umesh S.B.;Kumar H.
Proceedings of the International Conference on Computing Methodologies and Communication, ICCMC 2017, Volume 2018-January, Year 2018, Pages 200-203

10. A comparative study of different strategies using adaptive differential evolution for best scheduling in architectural level synthesis

Shilpa K.C , LakshmiNarayana .C
Indian Journal of Science and Technology, Volume 9, Year 2016

11. Adaptive particle swarm optimization for best schedule in algorithmic-

level synthesis

Proceedings of the International Conference on Communication and Electronics Systems, ICCES 2016, Volume , Year 2016

12. Adaptive Differential Evolution For Optimal Schedule In Behavioral Level Synthesis

Shilpa K.C
IOSR Journal of VLSI and Signal Processing (IOSR-JVSP), Volume 6, Year 2016, Pages 55-59

13. Natural computation for optimal scheduling with ILP modeling in high level synthesis

Procedia Computer Science, Volume 46, Year 2015, Pages 167-175

14. Scheduling in high level synthesis using discrete evolutionary programming

K. C. Shilpa ., C. Lakshminarayana ., M. K. Singh .,
2012 3rd International Conference on Computing, Communication and Networking Technologies, ICCCNT 2012, Volume , Year 2012