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Phone: , 09986007870

Address: ,Karnataka,India - 560056

Expertise

Electrical and Electronic Engineering

Digital System Design, IoT, VLSI, Programming Languages, Networking, Operating System

Work experience

1. Dr Ambedkar Institute of Technology, Bengaluru 2004 — Present

Assistant Professor (Grade-III)

Bengaluru

Education

1. M.Tech - 2009

R V College of Engineering

2. B.E. - 1996

Sir M V I T

Honours and Awards

1. Best Paper Award - 2022

International Conference

2. Silver Medal - 2020

MHRD NPTEL

Membership In Professional Bodies

1. LMISTE, 2006

Life time

2. cryptology research society of india, 2006

Life time

Membership In Committees

1. Extra Curricular Activity Committee, 2021

Memeber

2. NBA, 2018

Member

3. BOE, 2015

Memeber

4. BOS, 2010

Memeber

Publication

1. Design of efficient reversible floating-point arithmetic unit on field programmable gate array platform and its performance analysis

Girija S, Dr.Sangeetha B G

International Journal of Electrical and Computer Engineering , Volume 13, Year 2023, Pages 697-708

2. **Optimized 64-bit Reversible BCD Adder for Low-power Applications and Its Comparative Study**

Hemalatha K N, Girija S, Sangeetha B G

Proceedings of the International Conference on Computational Intelligence and Sustainable Technologies ICoCIST 2021, Volume , Year 2021, Pages

3. **Fault Tolerant Arithmetic and Logic Unit in Reversible Logic for Low Power Applications"**

Girija , Dr.Sangeetha B G

Design Engineering , Volume 8, Year 2021, Pages 1311-1323

4. **Real Conversation with Human-Machine 24/7 COVID-19 Chatbot Based on Knowledge Graph Contextual Search**

Dr. Tanuja Patgar, Ripal Patel, Girija S

Data Science and Computational Intelligence, Volume CCIS 1483, Year 2021, Pages 258-272

5. **An Insight into the Existing Reversible Arithmetic and Logical Units**

Girija S Dr. Sangeetha B G

Proceeding of Fifth International Conference on Microelectronics, Computing and Communication Systems, Volume 748, Year 2020, Pages 383-398

6. **Optimized Reversible Arithmetic and Logic Unit (ALU)**

Girija,S Prajwal A, Ramanju B V , Ranjith R, Supriya M

International Journal of Engineering Research and Applications, Volume 7, Year 2020, Pages 1084-1088

7. **Uav Hand-Over Entity**

Girija,S Prajwal A, Ramanju B V , Ranjith R, Supriya M

International Research Journal of Engineering and Technology, Volume 7, Year 2020, Pages 1084-1088

8. **ANTIBACTERIAL EFFECT OF SILVER NANOPARTICLES OBTAINED BY GREEN SYNTHESIS METHOD USING MILLETIA PINNATA AQUEOUS EXTRACT**

S. Vallimeena1 · B. Helina Jayanthi V.E

EPRA International Journal of Research and Development (IJRD), Volume 4, Year 2019, Pages 61-67

9. **Gesture recognition using RF signals**

Chandrakanth R. Bharath K.Bhargava U. G. Girija S.

International Journal of Advanced Research, Ideas and Innovation in Technology, Volume , Year 2019, Pages 332-335

10. **Ambulance Path Clearance for Smart City**

Bhavya Girija, S, Dheeraj N Kulal, AShwini Shetty, Aishwarya

International Conference on Recent Trends in Computational Engineering and Technologies, Volume 10, Year 2018, Pages 7

11. **Design Comparison Of Reversible Logic Flipflops For Future Quantum Computing**

Sreenikethana K V, Girija S, Hemalatha K N
ICISTSI proceedings 2015, Volume , Year 2015, Pages

12. **VHDL implementation of acquisition sensor for optical communication terminal**

Ashika B. P. Anuradha Prakash, Shri. Ravikumar K. Girija S.
International Journal of Advance Research, Ideas and Innovations In Technology, Volume , Pages