

## Mr Swamy TN

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## Expertise

#### **Electrical and Electronic Engineering**

VLSI and Image Processing

## Work experience

Dr Ambedkar Institute of Technology, Bengaluru 2013 — Present

Assistant Professor Bengaluru

### Education

1. Ph.D Persuing - 2023

Visvesvaraya Technological University

2. M.Tech - 2011

NITTE Meenakshi Institute of Technology

з. BE - 2008

#### Honours and Awards

 Elite Certificate for NBA Accreditation and Teaching Learning in Engineering - 2020

SWAYAM AICTE Roll No. NPTEL20GE11S11210364

 Departmental Coordinator for TEQIP-III Sponsored National Conference NCRTEIEC 2019 - 2019

Dr. Ambedkar Institute of Technology

3. Elite Certificate for Introduction to Research - 2019

SWAYAM AICTE Roll No> NPTEL19GE21S61142409

### Membership In Professional Bodies

- 1. International Association of Engineers (IAENG), 2021 Member 276518
- 2. IEEE, 2021 Graduate Student Member
- 3. Institute of Electronics and Telecommunication Engineers (IETE), 2019

  Member

## Membership In Committees

- 1. Department Level Academic Audit Commitee, 2022 Member
- 2. Department Level AICTE , 2020 Coordinator

- 3. Department Level VTU Coordinator, 2020
  Coordinator
- 4. ECE Department Magazine, 2020 Coordinator
- COVID-19 Task Force, 2020 Member
- 6. VTU Coordinator Committee, 2019
  Member
- 7. AICTE Coordinator(UG), 2019
  Coordinator

#### Patent

1. SYNTHESIS OF E-WASTE BASED ON MACHINE LEARNING & ARTIFICIAL INTELLIGENCE AUTONOMOUS MANAGEMENT SYSTEM FOR SMART ENVIRONMENT

Inventor(s): Swamy TN, Ramesha K, Assignee: Swamy TN, Ramesha K

Published: 2021-10-15

### **Publication**

 Improved Intra Prediction Algorithm for HEVC with Conventional and Convolutional Neural Network Approach

Swamy TN, Ramesha K International Journal of Mathematical Statistician and Engineering Applications, Volume 71, Year 2022, Pages 272- 280

Low Power Intra Prediction Architecture for HEVC Encoder using Deep Learning Approach

Swamy TN, Ramesha K International Journal of Current Science, Volume 12, Year 2022, Pages 24-31

3. MULTIMEDIA SENTIMENT ANALYSIS ON PUBLIC SOCIAL NETWORK USING NLP

Swamy TN, Lakshmi Janaki

## 4. DESIGN AND ASIC IMPLEMENTATION OF FOUR WAY SET-ASSOCIATIVE CACHE MEMORY

Swamy TN, Nusarath Jahan GB Sambodhi U GC Care Journal, Volume 44, Year 2021, Pages 202-207

#### Design and ASIC Implementation of Cache Controller of Four Way Set Associative Cache Memory

Swamy TN

International Conference on Intelligent Systems, Electrical and Communication Technology, Volume , Year 2021, Pages

#### 6. Early Cardiac Arrest Detection Using Thresholding Method

Swamy TN, Korgi Chinmai Shetty GIS SCIENCE JOURNAL, Volume 8, Year 2021, Pages 727-734

## 7. Video and image acquisition challenges to define image resolutions for HEVC

Mani Laxman Aiyar ., Ramesha Kenchappa ., International Journal of Advanced Science and Technology, Volume 29, Year 2020, Pages 958-965

#### 8. PERFORMANCE ANALYSIS OF BRAUN MULTIPLIER USING FULL ADDER

Swamy TN, Vani K

AICTE Sponsored 1st International E-Conference On "Recent Trends & Development in Information and Communication Engineering, Volume , Year 2020, Pages 228-

#### Power Efficient Algorithm for High Speed Bi-Directional Transceiver

Swamy TN

National Conference on Recent Trends in Science, Engineering and Management, Volume , Year 2019, Pages

#### Raspberry Pi Based Communication System for Deaf Dumb and Blind Person

Swamy TN, Manish AV

International Journal for Scientific Research & Development|, Volume 7, Year 2019, Pages 1464-1466

# 11. An efficient algorithm for intra prediction in HEVC/H.265 standard for $16 \times 16$ pixels

International Conference on Electrical, Electronics, Communication Computer Technologies and Optimization Techniques, ICEECCOT 2017, Volume 2018-January, Year 2018, Pages 226-233

#### 12. SINGLE CYCLE 64-BIT RISC-V PROCESSOR AND IT'S FPGA PROTOTYPE

Swamy TN, Pallavi S

Journal of Emerging Technologies and Innovative Research (JETIR) www.jetir.org, Volume 5, Year 2018, Pages 644-650

# 13. Design and Implementation of High Speed Bi-directional Transceiver for Low Power Applications

Swamy TN, Sujanya Kumari T

International Journal on Recent and Innovation Trends in Computing and Communication, Volume 5, Year 2017, Pages 709 – 712

## 14. A Dynamic Parallel and Pipelined Architecture for Intra Prediction in H.265 Standard

Swamy TN, Ramesha K and Diwakar KR

A Dynamic Parallel and Pipelined Architecture for Intra Prediction in H.265 Standard, Volume 5, Year 2017, Pages 1073-1077

#### 15. Design of High Speed Bi-directional Transceiver for Low Power Applications

Swamy TN

National Conference on Wireless Control and Communication Technologies NCWCT 2015, Volume , Year 2015, Pages

# 16. A New Technique to Image Splitting using submatrix for real time video streaming

Swamy TN

National Conference on VLSI, Communication and Signal Processing NCVCS-2015, Volume , Year 2015, Pages

## 17. FPGA Implementation of Efficient Algorithm of Image Water Marking using DWT

Swamy TN

National Conference on VLSI, Signal Processing, Communication and Soft Computing NCVCS-14, Volume , Year 2014, Pages

# 18. A New Technique to Digital Image Watermarking Using DWT for Real Time Applications

Swamy TN, Ramesha K and Cyril Prasanna Raj Int. Journal of Engineering Research and Applications, Volume 4, Year 2014, Pages 102-107

#### 19. Data Acquisition system based on FPGA

Swamy TN, Rashmi KM

International Journal of Engineering Research and Applications, Volume 3, Year 2013, Pages 1504-1509

# 20. FPGA Implementation Of Efficient Algorithm Of Image Splitting For Video Streaming Data

TN, Swamy and KM, Rashmi and Raj, P Cyril Prasanna and Pinjare SL

# 21. A Generic Standalone Design Approach to Embedded Hardware Development

Swamy TN, Pranav Jayaram

International Journal of Advances in Scientific Research and Engineering (IJASRE), Volume 5, Pages 41-48

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