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-  **Scopus Id**   
57264768700 (<http://www.scopus.com/authid/detail.url?authorId=57264768700>)

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Mr. Siddesha K ▾ ▾

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Mr Siddesha K 

Assistant Professor (Grade-I) 

Dr Ambedkar Institute of Technology, Bengaluru

Profile URL: <https://drait.irins.org/profile/354499> (<https://drait.irins.org/profile/354499>)

💡 Expertise



Electrical and Electronic Engineering

⚙ Embedded Systems, RTOS, Digital Communication, Image Processing

👤 Personal Information

Edit Information

Mr Siddesha K

👤 Male

👤 Contact Information

Add / Edit

🏠 #2, 2nd Cross, 2nd Block, Somanna Garden, Near HMT Layout, Vidyanarayapura, Bengaluru, Karnataka, India

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📞 080-23647689

🌐 ()

👜 Experience


Add

2012 - Present

Assistant Professor (Grade-I)


■ Dr Ambedkar Institute of Technology, Bengaluru, Bengaluru




 Education Qualification

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2012

 M.Tech.
Visvesvaraya Technological University



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A novel deep reinforcement learning scheme for task scheduling in cloud computing




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Siddesha K.;Jayaramaiah G.V.;Singh C.




article Cluster Computing, **Volume 25, Year 2022, Pages 4171-4188**



<http://dx.doi.org/10.1007/s10586-022-03630-2>
Energy Efficient Greedy Scheduling of Tasks for DVFS Enabled Heterogeneous Multicore Processors

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
Siddesha K.;Jayaramaiah G.V.

Conference Paper 2021 6th International Conference on Recent Trends on Electronics, Information, Communication and Technology, RTEICT 2021, **Volume , Year 2021, Pages 538-542**



<http://dx.doi.org/10.1109/RTEICT52294.2021.9573873>
Energy-Aware Task Scheduling Approach Using DVFS and Particle Swarm Optimization for Heterogeneous Multicore Processors

K Siddesha, G V Jayaramaiah

conference paper Emerging Research in Computing, Information, Communication and Applications, **Volume , Year 2021, Pages**



<http://dx.doi.org/>
Performance Analysis of Task Scheduling Algorithms for Energy Efficiency in Mobile Cloud Computing Environment

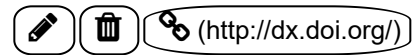
K Siddesha, B M Kavitha Narayana, Ramappa Hiremani

article Design Engineering, **Volume , Year 2021, Pages**



<http://dx.doi.org/>
Study and Performance analysis of different multiprocessor real time schedulers

Kavitha Narayana B M, Ramappa Hiremani, Siddesha K



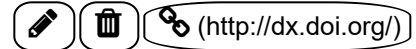
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Heterogeneous Processor Scheduling Using Adaptive Particle Swarm Optimization For DVFS Enabled Embedded Systems

K Siddesha, G V Jayaramaiah

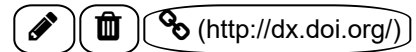
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A Review on Techniques for Power Management in Embedded Systems

K Siddesha, G V Jayaramaiah

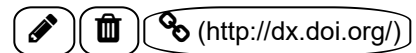
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Study and Implementation Of DVFS Technique for Processor Power Reduction

SIDDESHA K, RANJAN H K

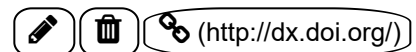
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Implementation of Real time based Automated Lighting Control System

Siddesha K, Kavitha Narayan B M, Nethravathi G Nelogal

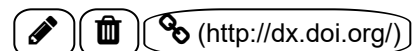
[article](#) International Journal of Advanced Research in Computer and Communication Engineering, **Volume** , **Year** 2018, **Pages**



Efficient Power Management Method for Embedded Processor Based on Processor's Bandwidth Control

Divya D, Siddesha K

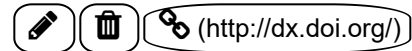
[article](#) International Journal of Engineering Science and Computing, **Volume** , **Year** 2016, **Pages**



A Methodology for Implementation of Smart Security system using Near Field Body Coupled Communication Technique

Siddesha K, Kavitha Narayan B M, Ranganath B

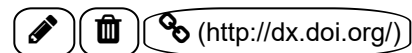
article International Journal of Advanced Research in Computer and Communication Engineering, **Volume**, **Year** 2016, **Pages**



RTL design and verification of SPI master-slave using UVM

D Roopesh, K Siddesha, Kavitha Narayan

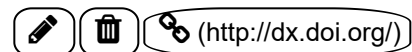
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Design of Virtex-7 FPGA based Data Communication Device

Anandaraj. D, Siddesha.K, Sofia Evelin, V.V Jagannadham

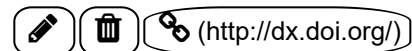
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Frequency domain based MSRCR method for color image enhancement

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